

## ME 493: Subsystem Design Planning

### Performance Verification

Metric

Target

Upper/lower limit

Achieved Performance

(value, not yes/no)

Verified by (team members)

Verified on date

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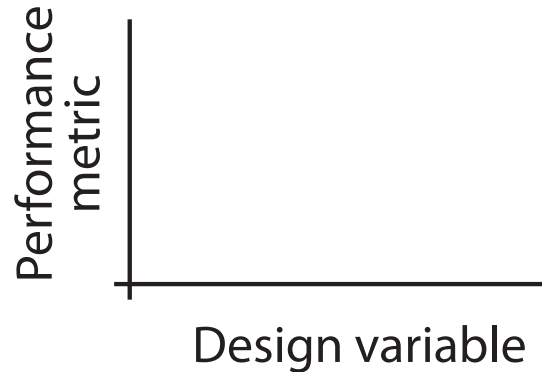
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### Design Activities to achieve performance target

Design task(s)

Design artifacts produced

Effort required to verify (hours)

Other resources: lab access, tools,  
software, purchased components

Team members assigned to task

Due date for completion

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