

CS410P/510 Programming Language Compilation Practice Midterm

Name: _____

Instructions

- This exam has 6 questions, for a total of 80 points.
- You may spend up to 1 hour, 50 minutes (110 minutes) on the exam.
- The exam is closed-book, closed-notes, except that one 8.5"x11" single-sided sheet of handwritten notes is permitted.
- No computing devices (laptops, tablets, cell phones, etc.) may be used.

Concrete syntax for all the intermediate languages mentioned in the exam can be found on the last two pages.

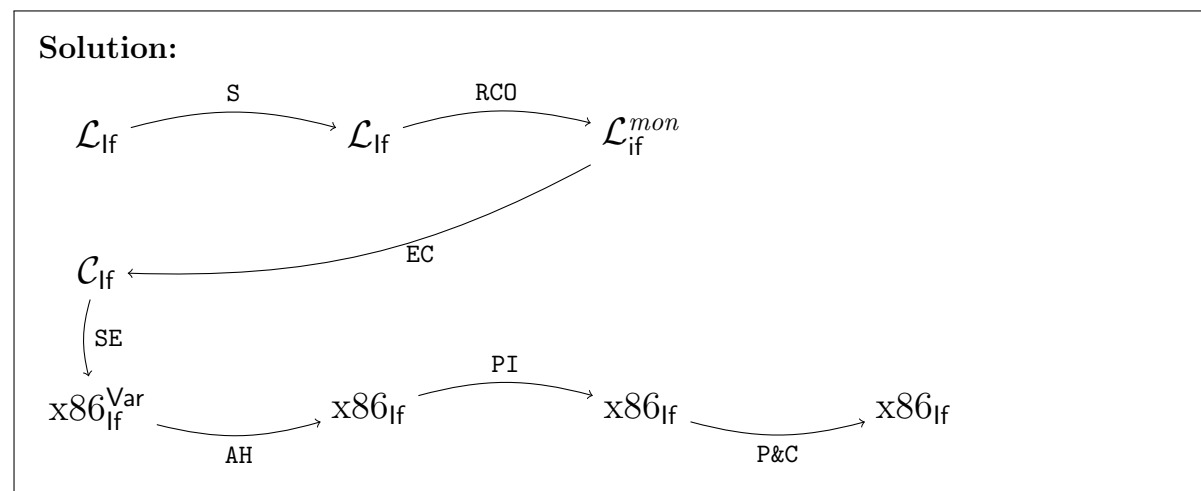
1. [10 points] Compiler Structure

The compiler for the language with booleans and conditionals in Chapter 5 involves multiple intermediate and target languages, namely: \mathcal{L}_{lf} (the source language for the whole compiler), $\mathcal{L}_{\text{lf}}^{\text{mon}}$, \mathcal{C}_{lf} , $\text{x86}_{\text{lf}}^{\text{Var}}$, and x86_{lf} (the target language for the whole compiler).

The compiler involves multiple passes, which are listed here (with abbreviations) in no particular order:

prelude_and_conclusion (P&C)
shrink (S)
select_instructions (SE)
explicate_control (EC)
remove_complex_operands (RCO)
patch_instructions (PI)
assign_homes (AH)

Draw a diagram that shows the order in which the passes actually execute, and indicates which language is the source and target of each pass. (Use the abbreviations to save writing.)



2. [15 points] Compile the following \mathcal{L}_{if} program to an equivalent program in the $\mathcal{L}_{\text{if}}^{\text{mon}}$ language.

```
a = input_int()
b = 3 + ((- a) - 7)
c = 42 if (b < 10) else (a + input_int())
print(c)
```

Solution:

```
a = input_int()
tmp.0 = -a
tmp.1 = tmp.0 - 7
b = 3 + tmp.1
c = 42 if b < 10 else
    { tmp.2 = input_int()
      produce (a + tmp.2)}
print(c)
```

3. [15 points] Given the following code for the body of an $x86_{\text{if}}^{\text{Var}}$ program written using symbolic variable names, write down the full assembly code for the $x86_{\text{if}}$ program obtained by assigning distinct `%rbp`-relative stack locations (not registers!) to the variables `x`, `t0`, and `t1`, in the style of Chapter 2. Your answer should be in the form of a single `main` function definition, given in the syntax of $x86_{\text{if}}$, i.e. the usual assembler syntax of `.s` files. Be sure to give the *complete* function code, including entry and exit sequences, and consisting entirely of *legal* instructions.

```

callq _read_int
movq %rax, x
movq $-7, t0
movq t0, t1
addq x, t1
movq t1, %rdi
callq _print_int

```

Solution: One possible solution. The assignment of the three variables to the three stack slots is arbitrary.

```

.globl main
main:
    pushq %rbp
    movq %rsp, %rbp
    subq $32, %rsp          ; rounding up to a multiple of 16
    callq _read_int
    movq %rax, -16(%rbp)    ; x : -16
    movq $-7, -8(%rbp)     ; t0 : -8
    movq -8(%rbp), %rax
    movq %rax, -24(%rbp)    ; t1: -24
    movq -16(%rbp), %rax
    addq %rax, -24(%rbp)
    movq -24(%rbp), %rdi
    callq _print_int
    movq $0, %rax          ; not essential
    addq $32, %rsp
    popq %rbp
    retq

```

4. [10 points] Recall that numeric comparisons on the X86 are performed by setting the condition codes (typically using a `cmpq` instruction) and then testing them using one of the `setcc` or `jcc` instructions. Our compiler finds it useful to generate both `setcc` and `jcc` instructions in different situations.

Illustrate why, by giving a *short* \mathcal{L}_{if} source program fragment and its translation into $\text{x86}_{\text{if}}^{\text{Var}}$, where the translated program uses both kinds of *cc*-testing instructions.

Solution: We use `setcc` to store the result of a comparison into a boolean variable, and `jcc` to implement the conditional jump associated with an `if` statement or expression.

Here's one simple example.

```
x = a < 2
if a > 3:
    b = 42
else:
    b = 99
```

Resulting $\text{x86}_{\text{if}}^{\text{Var}}$ code:

```
cmpq $2,a
setl %al
movzbq %al,x
cmpq $3,a
jg L2
movq $42,b
jmp L3
L2: movq $99,b
L3:
```

5. [15 points] For the following \mathcal{C}_{lf} program, fill in the live-after and live-before sets at each specified point in the program. (Note: although in our compiler we compute liveness information for X86 code, exactly the same ideas can be used to compute liveness for \mathcal{C}_{lf} code.)

<pre>start: a = 1 b = 2 t3 = input_int() if t3 == 0: goto block1 else: goto block2</pre>	<pre>live-before = live-after = live-after = live-after =</pre>
<pre>block1: t2 = a t3 = -t2 goto block3</pre>	<pre>live-before = live-after = live-after =</pre>
<pre>block2: t3 = b t4 = 20 goto block3</pre>	<pre>live-before = live-after = live-after =</pre>
<pre>block3: x = t3 t5 = x + 10 print(t5) return 0</pre>	<pre>live-before = live-after = live-after = live-after =</pre>

Solution:

	live-before = {}
start:	
a = 1	live-after = {a}
b = 2	live-after = {a,b}
t3 = input_int()	live-after = {a,b,t3}
if t3 == 0: goto block1	
else: goto block2	
	live-before = {a}
block1:	
t2 = a	live-after = {t2}
t3 = -t2	live-after = {t3}
goto block3	
	live-before = {b}
block2:	
t3 = b	live-after = {t3}
t4 = 20	live-after = {t3}
goto block3	
	live-before = {t3}
block3:	
x = t3	live-after = {x}
t5 = x + 10	live-after = {t5}
print(t5)	live-after = {}
return 0	

6. [15 points] Consider the following results from liveness analysis on a x86_{var} program using symbolic variable names, where the live-after set is listed next to each instruction.

```
start:
    callq    read_int    ; %rax
    movq     %rax, x     ; x
    movq     $1, y       ; x,y
    movq     $2, z       ; x,y,z
    movq     y, w        ; x,w,z
    addq     $2, w        ; x,w,z
    movq     z, t        ; x,w,t
    addq     w, t        ; t,x
    movq     t, %rax     ; %rax,x
    addq     x, %rax     ; %rax
    jmp      conclusion
```

- (a) Draw the interference graph for the variables **x,y,z,w,t**. (You can ignore **%rax**.)
- (b) What is the minimum number of locations (registers or stack slots) needed to hold the five variables in this code?

Solution:

- (a) [12 pts] Here are the edge adjacency lists for the graph:

```
t : w x
w : t x z
x : t w y z
y : x z
z : w x y
```

- (b) [3 pts] Three locations are necessary and sufficient. Sufficiency is shown by this assignment: **loc1:t,z**; **loc2:w,y**; **loc3:x**. Necessity follows because the graph contains several fully connected subgraphs (*cliques*), namely **t-w-x**, **x-w-z** and **x-z-y**. (A less precise argument is that it follows because there are multiple program points where three variables are simultaneously live. But some of these could conceivably share the same slot if they also share the same value, is in the **movq** special case we studied.)

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Concrete Syntax of Languages

\mathcal{L}_{if}

```

cmp ::= == | != | < | <= | > | >=
exp ::= int | bool | var
        | input_int() | - exp | not exp | exp + exp | exp - exp
        | exp and exp | exp or exp | (exp)
        | exp cmp exp | exp if exp else exp
stmt ::= print(exp) | exp | var = exp | if exp: stmt+ else: stmt+
 $\mathcal{L}_{\text{if}}$  ::= stmt*

```

$\mathcal{L}_{\text{if}}^{\text{mon}}$

```

atm ::= int | bool | var
cmp ::= == | != | < | <= | > | >=
exp ::= atm | input_int() | - atm | not atm | atm + atm | atm - atm
        | atm cmp atm | exp if exp else exp | {stmt* produce(exp)}
stmt ::= print(atm) | exp | var = exp | if exp: stmt+ else: stmt+
 $\mathcal{L}_{\text{if}}^{\text{mon}}$  ::= stmt*

```

Note: the concrete expression {*stmt*^{*} produce(*exp*)} corresponds to the AST form Begin(*stmt*^{*}, *exp*).

\mathcal{C}_{if}

```

atm ::= int | bool | var
cmp ::= == | != | < | <= | > | >=
exp ::= atm | input_int() | - atm | not atm | atm + atm | atm - atm
        | atm cmp atm
stmt ::= print(atm) | exp | var = exp
tail ::= return exp | goto label | if atm cmp atm: goto label else: goto label
 $\mathcal{C}_{\text{if}}$  ::= (label: stmt* tail)...

```

x86^{Var}_{lf}

```
reg ::= rsp | rbp | rax | rbx | rcx | rdx | rsi | rdi |  
      r8 | r9 | r10 | r11 | r12 | r13 | r14 | r15  
bytereg ::= ah | al | bh | bl | ch | cl | dh | dl  
arg ::= $int | %reg | %bytereg | int(%reg) | var  
cc ::= e | ne | l | le | g | ge  
instr ::= addq arg, arg | subq arg, arg | negq arg | movq arg, arg  
        | pushq arg | popq arg | callq label | retq  
        | xorq arg, arg | cmpq arg, arg | setcc arg | movzbq arg, arg  
        | jmp label | jcc label | label: instr  
x86Varlf ::= .globl main  
            main: instr...
```

Note: this is the same as x86_{lf}, below, except that *var* is allowed as an *arg*.

x86_{lf}

```
reg ::= rsp | rbp | rax | rbx | rcx | rdx | rsi | rdi |  
      r8 | r9 | r10 | r11 | r12 | r13 | r14 | r15  
bytereg ::= ah | al | bh | bl | ch | cl | dh | dl  
arg ::= $int | %reg | %bytereg | int(%reg)  
cc ::= e | ne | l | le | g | ge  
instr ::= addq arg, arg | subq arg, arg | negq arg | movq arg, arg  
        | pushq arg | popq arg | callq label | retq  
        | xorq arg, arg | cmpq arg, arg | setcc arg | movzbq arg, arg  
        | jmp label | jcc label | label: instr  
x86lf ::= .globl main  
            main: instr...
```