Evolving Quantum **Circuits** with Genetic Algorithms and through Exhaustive space search

Outline

- Technology
 - Constraints
- Design of new quantum primitives
- Evolutionary and Frame based Generator
- Exhaustive Search
- Results
- Future directions
- Possible projects Homework

Technology for Quantum Computing

What size of (binary) Quantum Computers can be build in year 2003?





What size of (binary) Quantum Computers can be build in year 2003?

• 7 bits



•dicarbonylcyclopentadienyl (perfluorobutadien-2-yl) iron (C₁₁H₅F₅O₂Fe)

•Each fluorine and 2 Carbones can be used for computation

5 qubit 215 Hz Q. Processor

(Vandersypen, Steffen, Breyta Yannoni, Cleve, and Chuang, 2000)



Source: IBM, Hot Chips Conference, 2000

Qbits, \neg bits

- In binary quantum logic, the notation for the superposition is $\alpha/0 > + \beta/1 >$.
- These intermediate states cannot be distinguished, rather a *measurement* will yield that the qubit is in one of the basis states, |0>, or |1>.
- The *probability* that a measurement of a qudit yields state |0> is |α|², and the probability is |β|² for state |1>. The sum of these probabilites is one. The absolute values are required, since in general, α and β are *complex quantities*.

Quantum Circuit Synthesis LS Technology dependent

Constraints based on the properties of physical implementation of quantum circuits(technology constraints, gate costs)

We would like to assume that any two quantum wires can interact, but we are limited by the realization (layout) constraints

Structure of atomic bonds in the molecule **determines neighborhoods** in the circuit.

This is similar to restricted routing in FPGA layout - <u>link</u> <u>between logic and layout synthesis</u> known from CMOS design **now appears in quantum.** **Decoherence** plays an important role \rightarrow minimization circuit length

Minimization of cascade's width but each bit counts (more critical than in reversible synthesis)

At first, we will be interested only in the so-called **"permutation circuits"** - their unitary quantum matrices are permutation matrices

One solution to layout constraint problem in quantum NMR computers is to take into account <u>in logic synthesis</u> <u>phase</u> only those gate and their placements that are <u>technology-realizable</u>

- Even if conceptually we use higher complexity gates, ultimately we have to build from 2-qubit gates.
- Another possibility is to assume only primitives for the future algorithms are only 2-qubit gates then the optimal circuit will be the *shortest*
- Bottom line is that basic gate in quantum logic is a 2*2 (2-qubit gate).
- 3*3 Toffoli, Fredkin, de Vos, Kerntopf, Margolus are not directly realizable as a primitive

Molecule - Driven Layout and Logic Synthesis



Quantum wires A and C are not neighbors



A schematics with two binary Toffoli gates

This is a result of our ESOP minimizer program, but this is not realizable in NMR for the above molecule, because there is no connection between A and C, for instance, in the molecule.

So we have to modify the schematics as follows





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Design of new (complex) quantum gates and their costs

Design a Toffoli Gate from 2-qbit quantum primitives



 V & V⁺ are root square of NOT and its hermitian (complex) conjugate such as V*V =NOT

• V:
$$\frac{(1+i)}{2} \begin{bmatrix} 1 & -i \\ -i & 1 \end{bmatrix}$$
 C_V: q-bit 2 unchanged
unless q-bit 1 equals
to 1

Example: Optimal Solution to Miller Function

 $(AC \oplus BC \oplus AB) \oplus (A \oplus B) = AC' \oplus AB \oplus BC'$



2-qubit quantum realization of Miller Gate



Cost in Gates: 7*1 = 7

Fredkin Gate build from Toffoli and Feynman gates



Transforms



Cost in Gates: 7*1 = 7



Evolutionary and Frame-hased gate generators

Genetic Algorithm

- A set of elements being modified according to evolutionary rules:
 - Selection (based on the fitness function)
 - Crossing Over
 - Mutation
 - Replication
- These operators are made in generation steps
- Process stops when the solution is found
- Important in GA
 - Encoding of the elements/individuals
 - Complex with a lot of parameters
 - Simple, task specific no parameters
 - Fitness function
 - Simple
 - Including layout specific constraints
 - Cost of gates

Circuit Encoding



GA for quantum circuit synthesis

• Set of elements: randomly generated qcircuits encoded in string representation



5PWSWWPPHWCPPWSWWP







Evaluation





Operations



Overview

Mutation	Gates Blocks	Position (block/circuit)
Cross-Over*	Segments	Experimental (unitary matrices)
Reproduction	Circuits	Best gates Best Circuits

* - for circuits having only same number of I/O



- SUS, Roulette wheel
- Fitness:



Frame-based search starting from Peres gate



 $C=1 \Rightarrow (a+b)'=a'b'$

Other frame search examples





 $A = a \oplus c$

 $B = a'b \oplus a'c \oplus bc$

 $C = ab \oplus ac \oplus bc$

Cost in Gates: 5*1 = 5

Exhaustive Search

Exhaustive gate search

- Searching all gates in a very limited space of permutation 10¹⁵⁻10¹⁸
- Up to 7 segments circuits
- 3 I/O circuits
- Comparing to gates such as Toffoli, Fredkin, de Vos, Kerntopf, etc.

Exhaustive gate search

- Idea: to look for all possible equivalent gates in a certain category
- Using specified gates in different technologies
- Find the minimal possible cost of the gate

Results

Unitary gate search examples

No starting set restriction



Generations: 10 Mutation rate: 0.3



Generations: 20 Mutation rate: 0.3

Other gates search



Generations: 100

Mutation rate: 0.4

Random circuit search



EPR producing circuit

Generations: 450

Mutation: 0.3

4 /PWWHW/P /PWWF/P /PWSW/P /PWFW/P /PSWW/P /PFWW/P /PSWW/P /PWSW/P



Random circuit search

"Send" circuit



Generations: 150

Mutation: 0.3

3 /PWWH/P /PWF/P /PFW/P /PHWW/P



Examples for Toffoli







Experimental results

Number of inputs per q- gate	Number of generations	рМ	рС	Real time (average 20 runs)	pM<0.2 Number of generations	Real time (average 20 runs)	Populatio n size
1 - input	<50	0.4	0.6	< 30 seconds	<100	< 1 minute	50
2 - inputs	<50	0.6	0.4	< 30 seconds	<100	< 1 minute	50
3 - inputs	50 - 200	0.6	0.6	<1 minute	<200	< 3 minutes	60

Experimental results (cont.)

Problem searched	Solution f F1/F6	found	Time of search in number of generations F1/F6
Toffoli	YES/YES		<2000 / <50000
Fredkin	YES/YES		<1000 / <75000
Margolus	YES/NO		<1000 / <100000





Generalized Ternary 3*3 Toffoli Gate









Generalized n*n Fredkin Gate



Future Perspectives





Lamarckian optimization



•An alternative approach to this problem can be the use of Lamarckian approach to the GA.

•When a solution is found, the genotype is modified in order to be more precise for a given term-wise polarity set and the given function.

•Consequently the search for this individual will induce smallest search space

Baldwinian learning



Projects and Homework

General directions

- All results from a GA should be averaged over 20 runs
- All details, files, precisions can be either asked directly or by email on <u>lukac242@netscape.net</u>
- Exceptionally good or bad results should be denoted apart
- All steps during exploration and experimentation should be precisely written down in a log book

Statistical analysis of non linearity in synthesized circuits

- During synthesis fitness of circuits is highly non linear and non proportional to the distance from the final gate
- Goal: analyze a set of known gates (provided) and make a statistical analysis on the changes of the fitness function of the gates
- Finally establishing a table of results where the known gates will be represented as curves of fitness function

Statistical analysis of non linearity in synthesized circuits

Example



- Fitness
- Error
- Cost

Evolving Fitness function for QC synthesis

- Inversely to the classical approach the goal is to synthesize a set of parameters fitting on the non linearity present in the fitness function evaluating quantum circuits
- Parameters evolved can be either taken from already existing fitness function or a completely new fitness function can be evolved

Evolving Fitness function for QC synthesis

• Example



- Encode parameters α,β,Π into chromosome Π is arbitrary function such as +, -, *, /, etc.
- Evolve them according to your own selected strategy
- You can choose any fitness function you want but minimum 2 parameters

Pareto optimality GA and QC synthesis

- We want to test how will a GA with Pareto optimal evaluation evolve new quantum circuits.
- Minimal parameters are the size of the circuit and the error as a measure of the distance from the goal. More parameters can be used as cost, complexity, etc.
- Use ranking method to select the best individuals to the next generation

Pareto optimality GA and QC synthesis

- Evolving circuit according to more than one parameter.
- Ranking is a method allowing to evaluate circuits on the numbers of won tournament



•All individuals are compared to all other in the population

•Mutation, Crossover as in standard GA Comparing all parameters one to one

Fitness assigning on the number of won fights

if
$$\sum_{i} (X_0 - X_j) \ge i$$
 Ran $k_0 + +, \forall X = [x_0, ..., x_i]$
else Ran k_0

qCirc(0)

Parameters

Exploring GA for QC by modifying parameters and settings

- By modifying parameters of the GA for QC synthesis it is possible to modify the behavior of the algorithm
- Here the goal is to explore in a methodic way combinations of parameters in the GA and observe the different behavior (deception, premature convergence)
- Parameters to be modified can be all such as mutation or crossover probability, population size, selection criteria, etc.

Exploring GA for QC by modifying parameters and settings

- Parameters to modify
 - Probabilities
 - Mutation
 - Crossover
 - Selection threshold
 - Selection pressure
 - Elitism
 - Roulette wheel
 - Universal Stochastic Sampling
 - Tournament
 - Threshold
 - Mixed generations
 - Comparing children with parents
 - Genetic operators
 - Mutation
 - Normal, bitwise, inversion,
 - Crossover
 - Normal, Double, Multi-point, Multi-parent



Individuals with Fitness $> \lambda$



Exhaustive search of Q-circuits

- Works by enumerating all possibilities of gates
 - Permutations
 - Wire allocations
 - Def: number of wires, number of blocks
- Goal: Make an extensive research for specified gates using this software.
- Classification according to different criteria:
 - Distance from the goal
 - Complexity
 - Cost



- A console interface
 - Input: file
 - Output: file
- All commands through the input file
- Available Monday on the class Web Page
 - Download the zipped package
 - Contents: exe file, source file, documentation and user's manual

!!!!GOOD LUCK!!!!