State Assignment
Hybercube Embedding

state groups :
\{2,5,12,17\}
\{2,6,17\}

wrong!
Fanin-Oriented (exam next state pair)

- The same present state causes transition to next state pair.

Add \( \frac{n}{2} \) to \( w(S_2,S_4) \) because of \( S_1 \)
FSM Optimization

State Transition Diagram:
- States: S1, S2, S3, S4
- Transitions: 01, 00, 11, -0, 1-, 10, -1

Combinational Logic Diagram:
- Inputs: PI, PS
- Outputs: PO, NS
- Logic Functions: v1, v2, u1, u2

Legend:
- PI: Primary Input
- PO: Primary Output
- PS: Primary States
- NS: Next States
Hypercube Embedding Technique

- Observation: one-hot encoding is the easiest to decode
  Am I in state 2, 5, 12 or 17?
  binary: \( x_4'x_3'x_2'x_1x_0' (00010) + \)
  \( x_4'x_3'x_2x_1'x_0 (00101) + \)
  \( x_4x_3x_2x_1'x_0' (01100) + \)
  \( x_4x_3'x_2'x_1'x_0 (10001) \)
  one hot: \( x_2 + x_5 + x_{12} + x_{17} \)
  But one hot uses too many flip flops.

- Exploit this observation
  1. two-level minimization after one hot encoding identifies useful state group for decoding
  2. assigning the states in each group to a single face of the hypercube allows a single product term to decode the group to states.
State Assignment

- Assign unique code to each state to produce logic-level description
  - utilize unassigned codes effectively as don’t cares
- Choice for S state machine
  - minimum-bit encoding
    \[ \log S \]
  - maximum-bit encoding
    - one-hot encoding
    - using one bit per state
    - something in between
- Modern techniques
  - hypercube embedding of face constraint derived for collections of states (Kiss, Nova)
  - adjacency embedding guided by weights derived between state pairs (Mustang)
### Example

**Ex: state machine**

<table>
<thead>
<tr>
<th>input</th>
<th>current-state</th>
<th>next state</th>
<th>output</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>start</td>
<td>S6</td>
<td>00</td>
</tr>
<tr>
<td>0</td>
<td>S2</td>
<td>S5</td>
<td>00</td>
</tr>
<tr>
<td>0</td>
<td>S3</td>
<td>S5</td>
<td>00</td>
</tr>
<tr>
<td>0</td>
<td>S4</td>
<td>S6</td>
<td>00</td>
</tr>
<tr>
<td>0</td>
<td>S5</td>
<td>start</td>
<td>10</td>
</tr>
<tr>
<td>0</td>
<td>S6</td>
<td>start</td>
<td>01</td>
</tr>
<tr>
<td>0</td>
<td>S7</td>
<td>S5</td>
<td>00</td>
</tr>
<tr>
<td>1</td>
<td>start</td>
<td>S4</td>
<td>01</td>
</tr>
<tr>
<td>1</td>
<td>S2</td>
<td>S3</td>
<td>10</td>
</tr>
<tr>
<td>1</td>
<td>S3</td>
<td>S7</td>
<td>10</td>
</tr>
<tr>
<td>1</td>
<td>S4</td>
<td>S6</td>
<td>10</td>
</tr>
<tr>
<td>1</td>
<td>S5</td>
<td>S2</td>
<td>00</td>
</tr>
<tr>
<td>1</td>
<td>S6</td>
<td>S2</td>
<td>00</td>
</tr>
<tr>
<td>1</td>
<td>S7</td>
<td>S6</td>
<td>00</td>
</tr>
</tbody>
</table>

**Symbolic Implicant**: represent a transition from one or more state to a next state under some input condition.
State Assignment

Symbolic cover representation is related to a multiple-valued logic. Positional cube notation: a $p$ multiple-valued logic is represented as $P$ bits

$$(V_1, V_2, ..., V_p)$$

Ex: $V = 5$ for 5-value logic

(00010)

represent a set of values by one string

$V = 2$ or $V = 4$

(01010)
State Assignment

Find a minimum multiple-valued-input cover
- espresso

Ex: A minimal multiple-valued-input cover

<p>| | | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0110001</td>
<td>0000100 00</td>
</tr>
<tr>
<td>0</td>
<td>1001000</td>
<td>0000010 00</td>
</tr>
<tr>
<td>1</td>
<td>0001001</td>
<td>0000010 10</td>
</tr>
</tbody>
</table>
State Assignment

Consider the first symbolic implicant

0 011001 | 0000100 00

- This implicant shows that input “0” means “state-2” or “state-3” or “state-7” into “state-5” and assert output “00”
- This example shows the effect of symbolic logic minimization is to group together the states that are mapped by some input into the same next-state and assert the same output.
- We call it “state group” if we give encodings to the states in the state group in adjacent binary logic and no other states in the group face, then the states group can be implemented as a cube.
State Assignment

- group face: the minimal dimension subspace containing the encoding assigned to that group.

Ex: 0010 0**0 group face
    0100
    0110
Hybercube Embedding

state groups:
\{2, 5, 12, 17\}
\{2, 6, 17\}

wrong!
Hybercube Embedding

• Advantage:
  – use two-level logic minimizer to identify good state group
  – almost all of the advantage of one-hot encoding, but fewer state-bit
Adjacency-Based State Assignment

Basic algorithm:
(1) Assign weight \( w(s,t) \) to each pair of states
   - weight reflects desire of placing states adjacent on the hypercube
(2) Define cost function for assignment of codes to the states
   - penalize weights for the distance between the state code
     
     eg. \( w(s,t) \times \text{distance}(\text{enc}(s),\text{enc}(t)) \)
(3) Find assignment of codes which minimize this cost function summed over all pairs of states.
   - heuristic to find initial solution
   - pairwise interchange (simulated annealing) to improve solution
Adjacency-Based State Assignment

- Mustang: weight assignment technique based on loosely maximizing common cube factors
How to Assign Weight to State Pair

- Assign weights to state pairs based on ability to extract a common-cube factor if these two states are adjacent on the hybercube.
Fan-Out-Oriented
(examine present-state pairs)

- Present state pair transition to the same next state

\[ S_1 \quad S_3 \]
\[ \quad S_2 \]

\[ $$$ S_1 \quad S_2 $$$\$

\[ $$$ S_3 \quad S_2 $$$\$

Add \( n \) to \( w(S_1,S_3) \) because of \( S_2 \)
Fan-Out-Oriented

- present states pair asserts the same output

Add 1 to $w(S_1, S_3)$ because of output $j$
Fanin-Oriented (exam next state pair)

- The same present state causes transition to next state pair.

```
S1
```

```
S2  S3
```

Add n/2 to w(S₂, S₄) because of S₁
Fanin-Oriented (exam next state pair)

- The same input causes transition to next state pair.

\[
\begin{align*}
S_1 & \quad S_3 \\
\downarrow i & \quad \downarrow i \\
S_2 & \quad S_4
\end{align*}
\]

\[
\begin{align*}
0 & \quad S_1 \quad S_2 \\
0 & \quad S_3 \quad S_4
\end{align*}
\]

Add 1 to \(w(S_2,S_4)\) because of input 2
Which Method Is Better?

- Which is better?
  FSMs have no useful two-level face constraints => adjacency-embedding
  FSMs have many two-level face constraints => face-embedding