WINTER 2013

Course: ECE 221 Electric Circuits　　　　　**Time/Location:** MT 16.40-18.10 / FAB-10
　　　　　　(Note: Days and location have changed since registration first opened.)
Instructor: James Morris　　　　　　　　　　**Office**: FAB 160-13
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Office Hours: Mon 3–4 pm (& Tues 9-10am.) I will be glad to meet with you at other times by appointment if you have a conflict then.

Overview: This course is an introduction to dc electric circuit analysis. We cover basic analysis of resistive circuits using Ohm's and Kirchoff's laws and then learn simplifying techniques such as series and parallel resistance. The important circuit analysis concepts of node and loop analysis, superposition and equivalent circuits are emphasized. Lastly, we introduce inductors, capacitors and ideal op amps. The course outcomes are:

　　　1) the ability to analyze resistive circuits using basic laws;

　　　2) the ability to perform node and loop analysis;

　　　3) the ability to apply circuit theorems;

　　　4) the ability to analyze circuits containing ideal operational amplifiers;

　　　5) understanding the behavior of inductors and capacitors;

Text: Electric Circuits, Nilsson & Riedel, (Prentice-Hall) 9th edition, (either hard copy or e-book)

Tutors: Unfortunately there is no lecture TA or recitation section. There is free tutoring available from the IEEE student section, go to:
http://www.ece.pdx.edu/student_resources/IEEE_tutoring.php for times and locations. You're also encouraged to come to my office hours to get help on homework problems and answers to questions we don't have time for in class.

Online Resources - D2L: Course information including lecture notes, occasional handouts, homework solutions, exam reviews and grades will be on D2L (http://d2l.pdx.edu/.) If you are new to D2L, go to http://psuonline.pdx.edu/ to get started. You will need an Odin account, and you should be automatically added to the D2L course when you register for this class.

The lecture notes were written by a succession of instructors and I may not follow them exactly, but they are an excellent resource and you are highly encouraged to print them out and bring them to class to reduce note-taking.

Online Resources – MasteringEngineering and Homework: MasteringEngineering (ME) is a resource provided by the textbook publisher and requires that you purchase an access code. This textbook and the access code are used for all of 221/2/3. You need to register on ME at www.masteringengineering.com using the access code that comes with the new textbook or that you purchase separately. *Please do this within the next couple of days so that any registration problems can be sorted out this week.* When you get registered, you will see the ECE 221 course. The course ID is **ECE221W13**. There is an "Introduction to ME" assignment there already which

you should work through to get used to the system. Later assignments will be posted as they are ready. Please let me know if you have any problems!

ME is designed to help you learn the material and do the homework problems. There are interactive and video tutorials, homework hints and immediate feedback on whether your answer is correct. This is the third year we are using ME for this class. There are some quirks which can lead to some frustration, but the majority of past students recommended using it again. Here are some lessons learned from last year:

- Problems on ME usually have different numbers than those in the book.

- You have 10 attempts to enter the correct answer; don't burn through them all just trying things randomly! If you are stuck, get help before your attempts are used up.

- It is possible there is an error in the solution on ME, but it is far more likely you've made an error. If you really think the solution is wrong, let me know, but check your work carefully first.

- Be careful of rounding errors in repeated calculations; you can round off your *solution,* but carry more decimal places through the calculations.

- Be careful of specific directions. ME often asks for answers in specific units or format.

Homework will be assigned according to the schedule below and is due at 4.30pm on the day indicated (i.e. before class.) My plan is that you will do most of the homework on ME, although some, particularly LTSpice simulations, may be submitted at the start of class. Solutions will be posted after assignments are turned in. Late homework will not be accepted without a good reason.

Doing the homework is extremely important in this class - the only way to learn the techniques of circuit analysis is by practice! Doing the homework will help you to keep up with the class (you *don't* want to be trying to learn this material the night before the midterm!) and to be prepared for the types of problems you will see on the exams.

Online Resources – Recorded Lectures: This class is NOT recorded.

Software: The program LTSpice will be introduced in this course and used in homework assignments as well as in the lab. LTSpice is also called SwitcherCad III. It is a free program available from Linear Technology at http://www.linear.com/company/software.jsp. You can easily download it to your own computer. There is not a lot of documentation available from LT itself, but there is a lot of other web support. There is a Yahoo group for LTSpice at http://groups.yahoo.com/group/LTspice/. They have many files for download, including several tutorials and an extensive (290+ page) manual.

MATLAB (www.mathworks.com) will also be used, though primarily in the lab. More information on MATLAB is in Lab 2 and on LTSpice in Lab 4.

Also, if you don't have one yet, now is a good time to invest in a good scientific calculator. You will need one that solves linear simultaneous equations for this class.

Lab: Information on labs is in the lab syllabus. Although you register for them separately, the lab is an integral part of this course and it is required that the lab and lecture be taken together. You will get the same grade for both with the lab being 20% of the total grade.

Exams: Exams are closed book, with one formula sheet allowed. The sheet can contain formulas only, no worked examples, and will be turned in with the exam. If you have to miss an exam you must discuss it with me beforehand if at all possible; make-ups will only be given for compelling reasons.

If you are a student with a documented disability and registered with the Disability Resource Center (DRC), please contact me well before the first midterm to make appropriate accommodations. DRC can be reached at 725-4150.

Grading: If the class average is > 80%, grading is done by standard percentages, i.e. 90 and above = A, 80 - 89 = B, 70 - 79 = C, etc. If the class average is < 80%, a standard curve is used. Plus and minus grades are used for borderline cases at the instructor's discretion. Your evaluation will be based as follows:

> Homework --------------------------------- 10%
> Midterm #1 (Chapters 1 – 3) ----------- 20%
> Midterm #2 (Chapter 4) ----------------- 20%
> Final exam (Chapters 5 & 6) ------------ 30%
> Lab -- 20%

Academic Honesty: We take academic honesty very seriously. Our department policy is to report all instances of plagiarism or cheating to the university. If you are not sure what constitutes plagiarism, ask and we'll talk about it. Simply put – turn in only your own work or credit the source.

Schedule: The following schedule is subject to change; if you miss class, check D2L to verify exam dates.

Week	Dates	Monday	Tuesday
1	1/7-8	Introduction; 1.1-1.2 (Assign #1)	1.3-1.6 Circuit Variables (Assign #2)
2	1/14-15	2.1-2.3 Circuit Elements (#1, 2 due)	2.4-2.5 Kirchoff's Laws (Assign #3)
3	1/21-22	**No Class (MLK)**	3.1-3.4 Resistive Circuits (#3 due)
4	1/28-29	3.5-3.6 Measurement; Δ-Y (Assign #4)	4.1-4.4 Node Mesh Analysis
5	2/4-5	<u>Review</u> Chapters 1-3 (#4 due)	**Midterm #1** (Chapters 1-3)
6	2/11-12	4.5-4.8 Mesh Analysis (Assign #5)	4.9-4.11 Thevenin/Norton
7	2/18-19	4.12-4.13 Circuit Theorems (#5 due, Assign #6)	5.1-5.5 Operational Amplifiers I
8	2/25-26	<u>Review</u> Chapter 4 (#6 due)	**Midterm #2** (Chapter 4)
9	3/4-5	5.6-5.7 OpAmps II (Assign #7)	6.1-6.3 Capacitors & Inductors
10	3/11-12	6.4-6.5 Mutual Inductance (#7 due, Assign #8)	<u>Review</u> Chapters 5-6 (#8 due 4.30pm Fri 15th)

Final Exam   **Monday 18th March**   **5.30-7.20pm**   (Chapters 5 & 6, plus 1-4)