List of class projects

1. Placement approaches and algorithms for over million transistor circuits
2. Three-dimensional Placement
3. System in Package, methodology and CAD tools
4. Clock routing approach and algorithms for low-power applications
5. Power and ground routing for improved reliability
6. Power and ground routing for reduced noise (decoupling capacitors)
7. New Partitioning algorithms for low-power applications
8. Layout issues for new nano-technologies
9. Algorithms and methodologies for injecting repeaters into long interconnects
10. Issues in SOC and MS-SOC layouts