

Infix / Postfix Notation

Consider Binary Operators

Infix Notation: $\text{operand operator operand}$

Can be ambiguous!

$$\begin{array}{c} \xrightarrow{\hspace{1cm}} x + (y - z) \\ x + y - z \\ \xrightarrow{\hspace{1cm}} (x + y) - z \end{array}$$

Need rules of precedence, associativity, parentheses.

Postfix Notation: $\text{operand operand operator}$

Eliminates ambiguity!

$$\begin{array}{l} x\ y\ z\ -\ + \\ x\ y\ +\ z\ - \end{array} \quad \begin{array}{l} \text{means } x + (y - z) \\ \text{means } (x + y) - z \end{array}$$

Assumption: No confusion about how many operands an operator requires.

binary- versus unary-

Infix: $x + -(y - z)$	$x + (y - -z)$
Postfix: $x\ y\ z\ -_{\text{bin}}\ -_{\text{un}}\ +$	$x\ y\ z\ -_{\text{un}}\ -_{\text{bin}}\ +$

Converting Expressions to Postfix

Let E be an infix expression.

Define $\text{POSTFIX}(E)$ to be the same expression in postfix.

(Ignore unary operators.)

- If E is a variable or constant...

then $\text{POSTFIX}(E) = E$
- If E is of the form $E_1 \text{ op } E_2 \dots$

then $\text{POSTFIX}(E_1 \text{ op } E_2) = \text{POSTFIX}(E_1) \parallel \text{POSTFIX}(E_2) \parallel \text{op}$
- If E is of the form $(E_1) \dots$

then $\text{POSTFIX}((E_1)) = \text{POSTFIX}(E_1)$

String concatenation

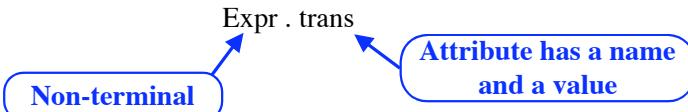
Syntax-Directed Translation

Problem/Goal:

Translate infix expressions into postfix.
The input is described by a CFG.

Approach:

Start with the grammar.
Each production is augmented with *semantic rules*.
Each non-terminal has an associated *attribute*.



Semantic rules added to grammar productions
...tell how to compute the attributes' values.

`trans = "a b + c +";`

An Example Attribute Grammar

CFG Grammar

Expr → Expr + Term
→ Expr - Term
→ Term
Term → ID

Terminals:

“+”, “-”, ID
Token attribute: ID.svalue

Non-terminals:

Expr
Term

Attributes:

Expr.t
Term.s

Attribute Values:

Strings, e.g., “x y + z -”
Concatenation: ||

Attribute Grammar

$\text{Expr} \rightarrow \text{Expr} + \text{Term}$

$\text{Expr} \rightarrow \text{Expr} - \text{Term}$

$\text{Expr} \rightarrow \text{Term}$

$\text{Term} \rightarrow \text{ID}$

Attribute Grammar

$\text{Expr}_0 \rightarrow \text{Expr}_1 + \text{Term}$

$\text{Expr}_0 \rightarrow \text{Expr}_1 - \text{Term}$

$\text{Expr}_0 \rightarrow \text{Term}$

$\text{Term} \rightarrow \text{ID}$

Subscripts added
*...to tell different
non-terminals apart*

Attribute Grammar

$\text{Expr}_0 \rightarrow \text{Expr}_1 + \text{Term}$

`Expr0.t = Expr1.t || Term.s || "+";`

$\text{Expr}_0 \rightarrow \text{Expr}_1 - \text{Term}$

`Expr0.t = Expr1.t || Term.s || "-";`

$\text{Expr}_0 \rightarrow \text{Term}$

`Expr0.t = Term.s;`

$\text{Term} \rightarrow \text{ID}$

`Term.s = ID.svalue;`

Example

Translate: “**X - Y + W**”

Expr_0	$\rightarrow \text{Expr}_1 + \text{Term}$
Expr_0	$\rightarrow \text{Expr}_1 - \text{Term}$
Expr_0	$\rightarrow \text{Term}$
Term	$\rightarrow \text{ID}$

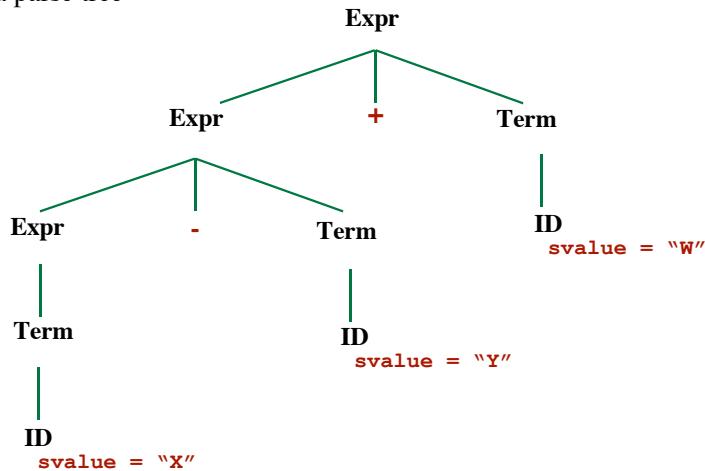
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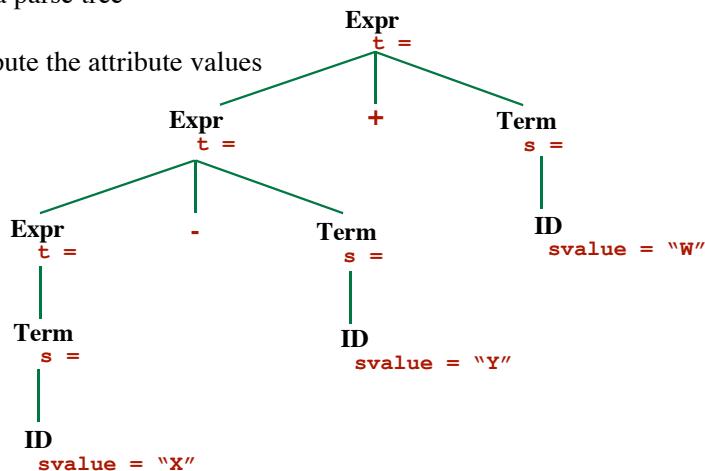
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Step 1: Find a parse tree

Step 2: Compute the attribute values



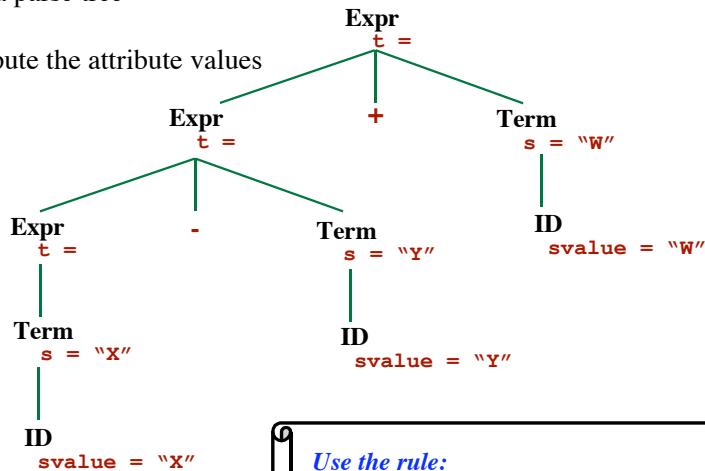
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Use the rule:

$\text{Term} \rightarrow \text{ID} \quad \text{Term}.s = \text{ID}.svalue;$

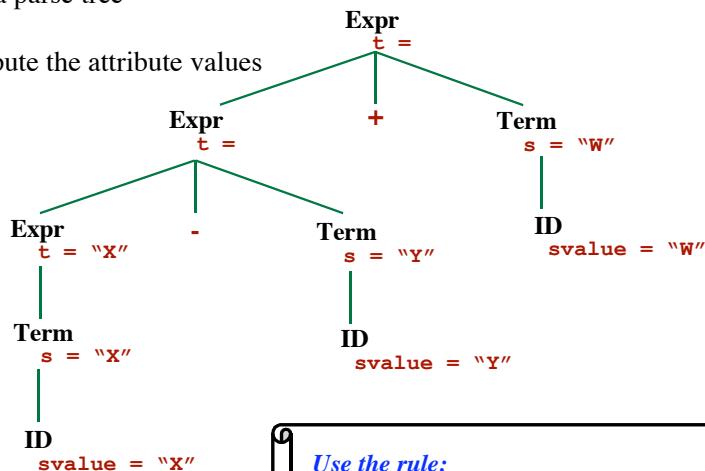
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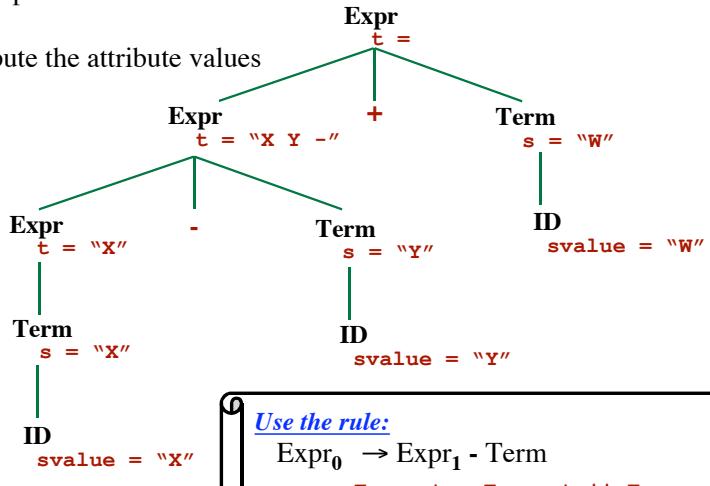
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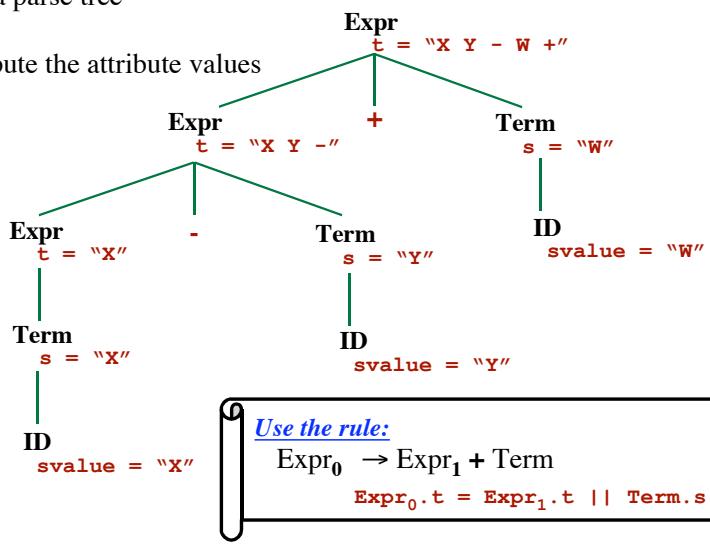
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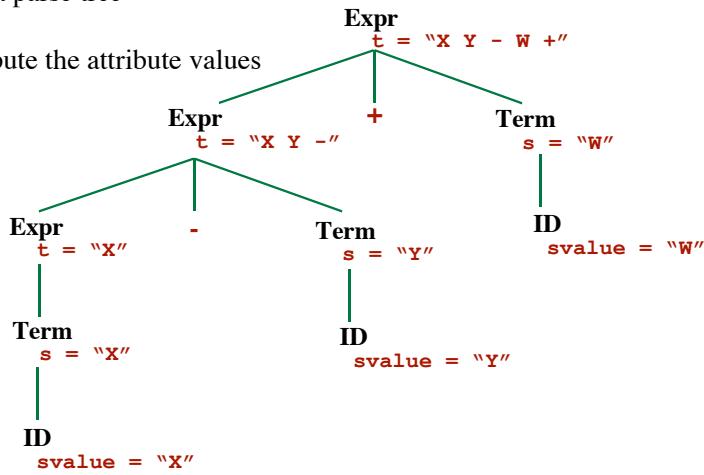
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Synthesized v. Inherited

Synthesized Attributes

(see previous example)

Compute the attributes bottom-up

From leaves toward root

Example Semantic Rule:

$\text{Expr}_0 \rightarrow \text{Expr}_1 - \text{Term} \quad \text{Expr}_0.t = \text{Expr}_1.t || \text{Term}.s || "-";$

All rules compute the attribute of the left-hand side

... as a function of the attributes from the right-hand side.

$X \rightarrow A B C \quad x.t = f(A.t, B.t, C.t);$

Information flows *up the tree*.

A *Bottom-Up* Approach

Inherited Attributes

Information flows *down the tree*

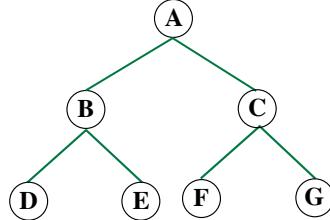
Example:

$X \rightarrow A B C \quad B.t = f(x.t);$

A *Top-Down* Approach

Depth-First Traversal

```
function Visit (N: Node)
  for each child of N do
    Visit (child)
  endFor
  "process" N
endFunction
```



Sythesized Attributes

Evaluate children first
 Then move up the tree
 ... and take care of parents' attributes

Translator Schemes

Embed semantic actions into grammar rules.

Example

X → A { print(“+”) } B { print(“.”) } C

Enclose actions in braces { ... }

Arbitrary code (e.g., Java statements)

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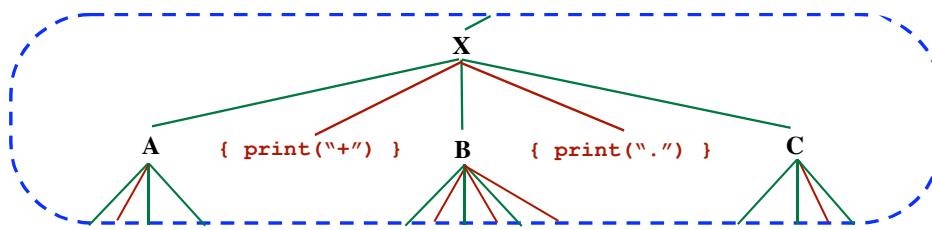
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Step 1: Construct a parse tree

Add the actions to the parse tree



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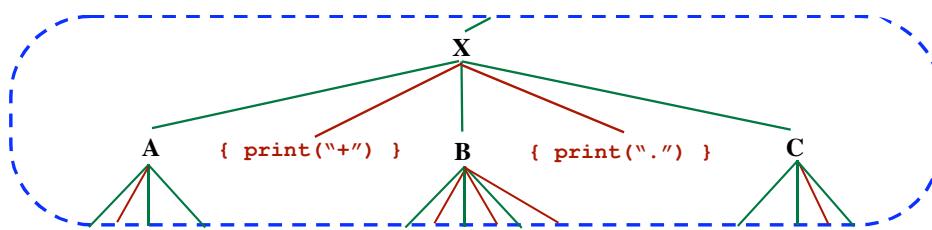
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Add the actions to the parse tree



Step 2: Perform a depth-first traversal

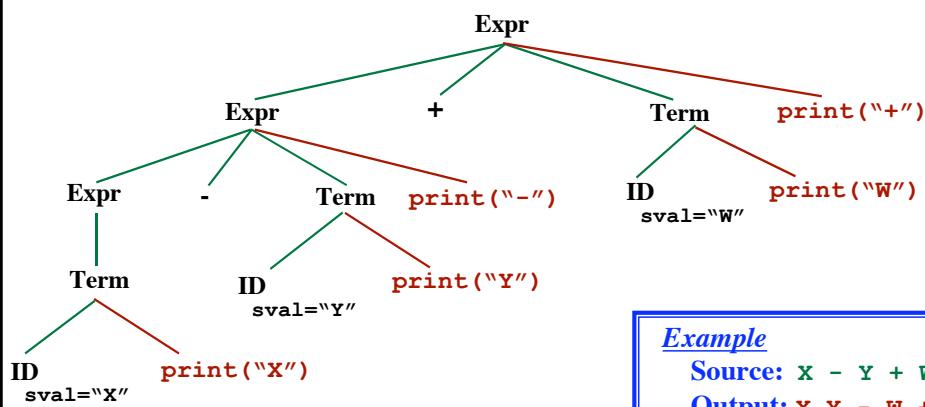
Execute actions as they are encountered in traversal

Example: Convert Infix Expressions to Postfix

Expr → Expr + Term { **print("+"**) }
 Expr → Expr - Term { **print("-")** }
 Expr → Term
 Term → ID { **print(ID.svalue)** }

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**Example**

Source: `X - Y + W`
Output: `X Y - W +`

Introduction to Compiling - Part 2

Assume we have a translator scheme...

Assume we have a parser...

Can we execute the actions while we do the parsing?

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Depth-first traversal → Recursive descent parser!

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      → Term  
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```

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Assume we have a translator scheme...

Assume we have a parser...

Can we execute the actions while we do the parsing?

Depth-first traversal → Recursive descent parser!

Example:

```
Expr → Expr + Term { print(“+”) }  
      → Expr - Term { print(“-”) }  
      → Term  
Term → ID { print(ID.svalue) }
```

First, we'll need to eliminate left-recursion:

```
Expr → Term Rest  
Rest → + Term { print(“+”) } Rest  
      → - Term { print(“-”) } Rest  
      → ε  
Term → ID { print(ID.svalue) }
```

Implementation

```

Expr → Term Rest
Rest → + Term { print(“+”) } Rest
      → - Term { print(“-”) } Rest
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Term → ID { print(ID.svalue) }
  
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```

function ParseExpr ()
  ParseTerm ()
  ParseRest ()
endFunction
  
```

Implementation

```

Expr → Term Rest
Rest → + Term { print(“+”) } Rest
      → - Term { print(“-”) } Rest
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Term → ID { print(ID.svalue) }
  
```

```

function ParseTerm ()
  if nextToken == ID then
    s = token.svalue
    MustHave (ID)
    print (s)
  else
    Error “Expecting ID”
  endif
endFunction
  
```

Implementation

```

Expr → Term Rest
Rest → + Term { print("+) } Rest
      → - Term { print("-") } Rest
      → ε
Term → ID { print(ID.svalue) }

```

```

function ParseRest ()
  if nextToken == '+' then
    MustHave ('+')
    ParseTerm ()
    print ("+")
    ParseRest ()
  elseIf nextToken == '-' then
    MustHave ('-')
    ParseTerm ()
    print ("")
    ParseRest ()
  else
    // Epsilon -- do nothing
  endIf
endFunction

```

Repeating...

```

function ParseExpr ()
  ParseTerm ()
  ParseRest ()
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function ParseRest ()
  if nextToken == '+' then
    MustHave ('+')
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Optimizing “Tail Recursion”

```

function ParseExpr ()
    ParseTerm ()
    ParseRest ()
endFunction

function ParseRest ()
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        MustHave ('-')
        ParseTerm ()
        print ("-")
        ParseRest ()
    else
        // Epsilon -- do nothing
    endIf
endFunction

function ParseRest ()
    while true
        if nextToken == '+' then
            MustHave ('+')
            ParseTerm ()
            print ("+")
        elseif nextToken == '-' then
            MustHave ('-')
            ParseTerm ()
            print ("-")
        else
            return
        endIf
    endwhile
endFunction

```

In-Lining...

```

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Generating Target Code

Output of compiler

- Assembly code (e.g., SPARC code)
- Machine code (e.g., 0x3b4E0F0F...)
- “Bytecodes” (e.g., PUSH, POP, GOTO, ...)

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Bytecodes

Higher level than machine-specific code.

Example: Java Bytecodes

Software to execute the instructions

 Interpreter (aka: “Virtual Machine”, “Emulator”)

Or translate the bytecodes into machine-specific code

 “Just-in-time” compilers

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Abstract Stack Machine

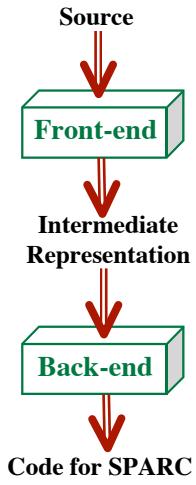
A virtual machine architecture

Based on a stack

Can be used as intermediate code

*Code for
Abstract Stack Machine*

*Replace with back-end
For Intel, Power-PC, ...*



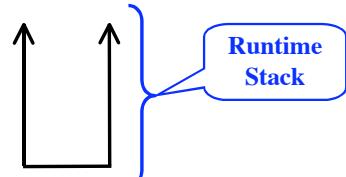
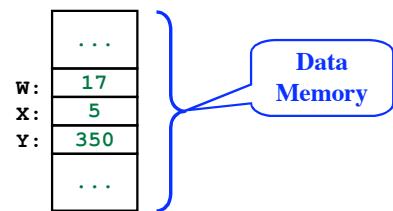
Abstract Stack Machine

- Limited / simple instruction set
- No registers
- Stack of data values
- Program memory
- Data memory

$$(3 + (6 - X))$$

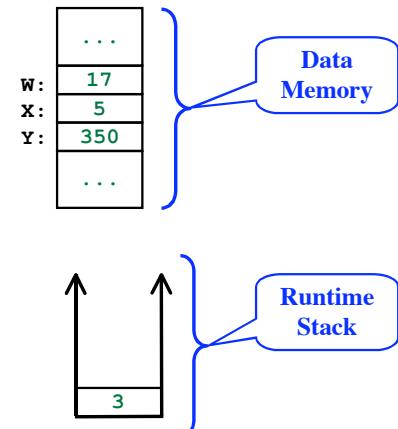
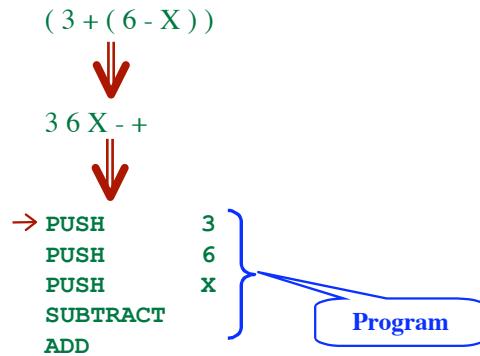


PUSH
PUSH
PUSH
SUBTRACT
ADD



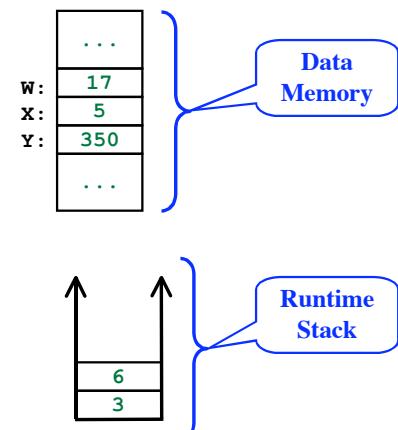
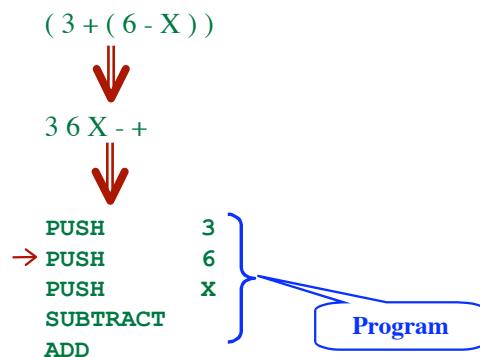
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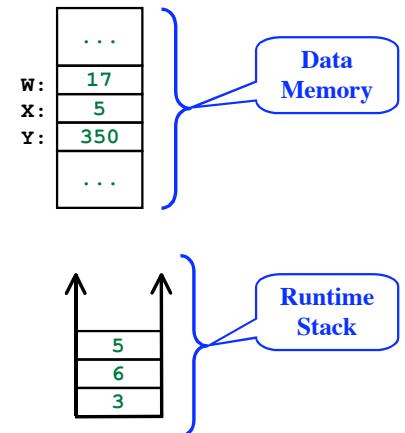
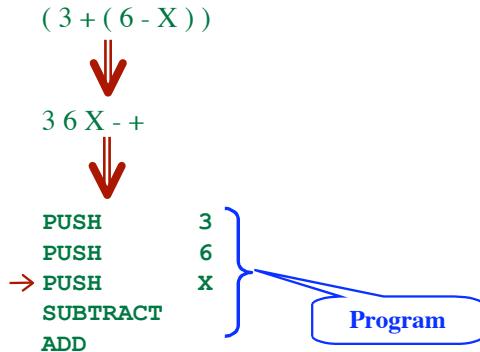
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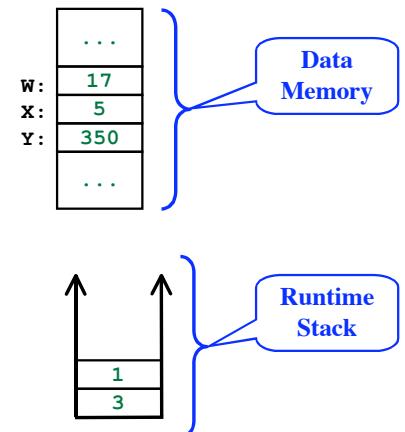
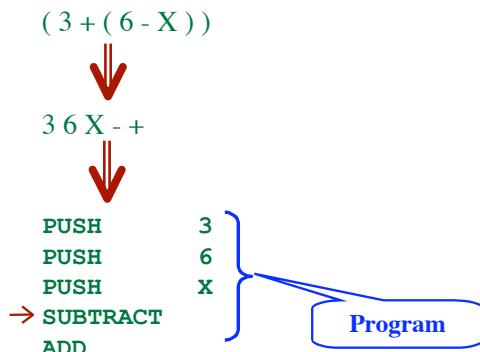
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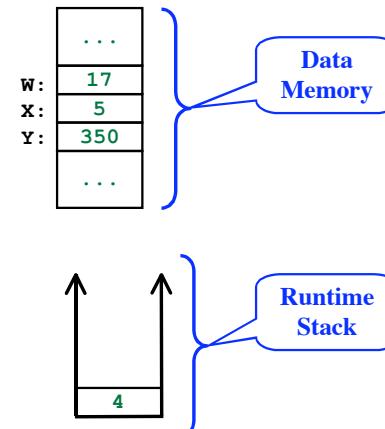
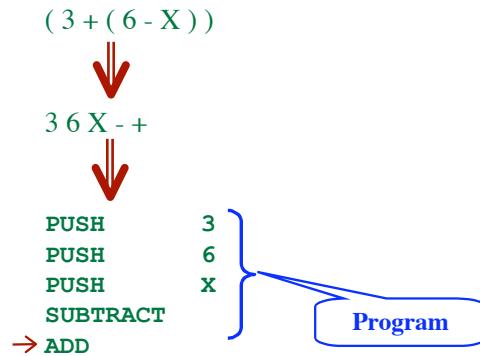
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L-Values versus R-Values

L-Value:

- Need the variable's *location*.

R-Value:

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Example:

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x = y * (z + 5);
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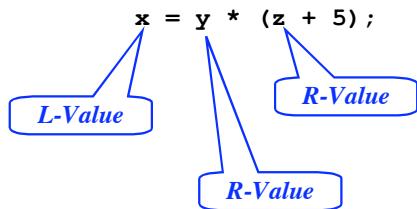
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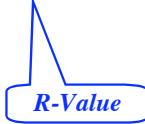
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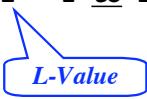
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L-Value

Stack Machine Instructions

Arithmetic Instructions

```
ADD
SUB
MULT
DIV
...etc...
```

Stack/Data Manipulation Instructions

```
PUSH    N
RVALUE  N
LVALUE  N
ASSIGN
POP
COPY
```

Flow of Control Instructions

```
GOTO    L
LABEL   L
GOFALSE L
GOTRUE  L
HALT
```

Stack Machine Instructions

Arithmetic Instructions

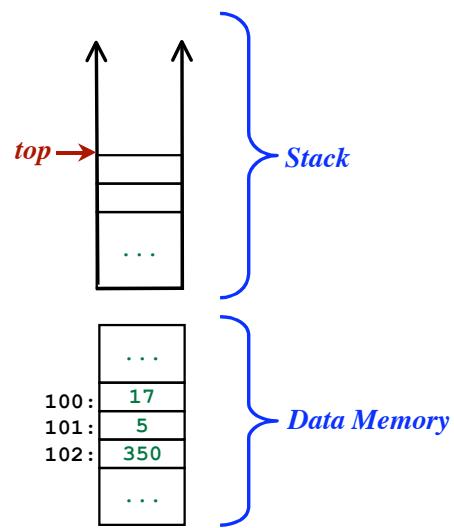
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...etc...

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RVALUE N
LVALUE N
ASSIGN
POP
COPY

Flow of Control Instructions

GOTO L
LABEL L
GOFALSE L
GOTRUE L
HALT



Stack Machine Instructions

Arithmetic Instructions

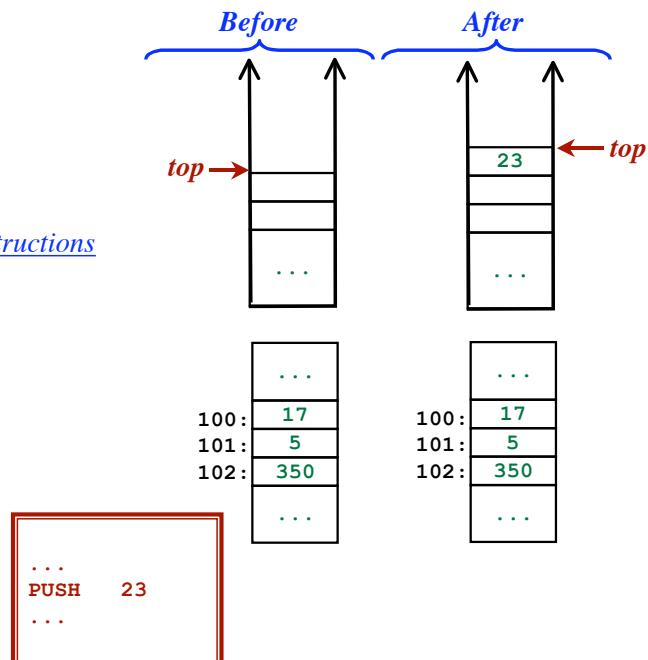
ADD
SUB
MULT
DIV
...etc...

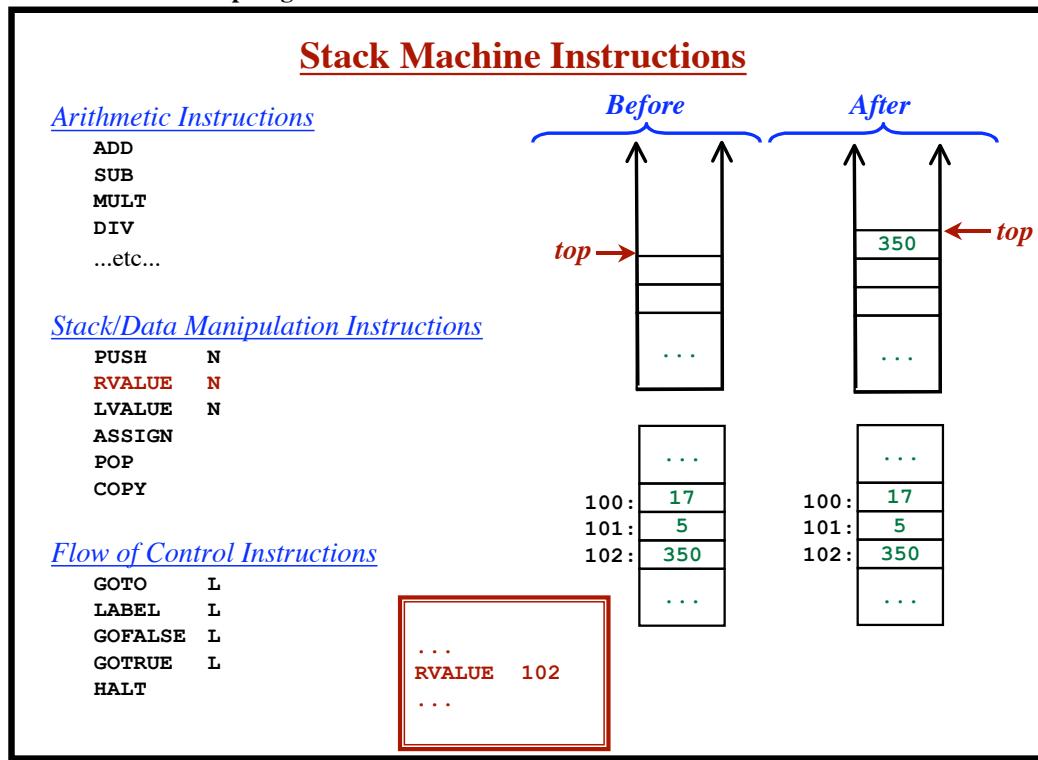
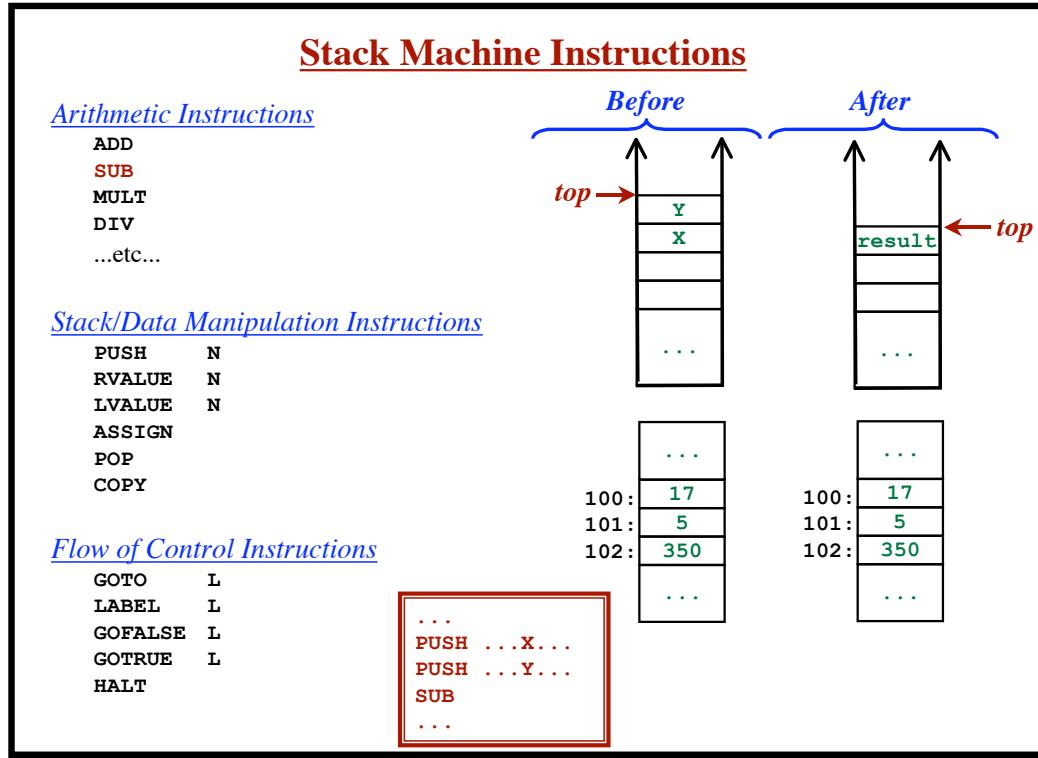
Stack/Data Manipulation Instructions

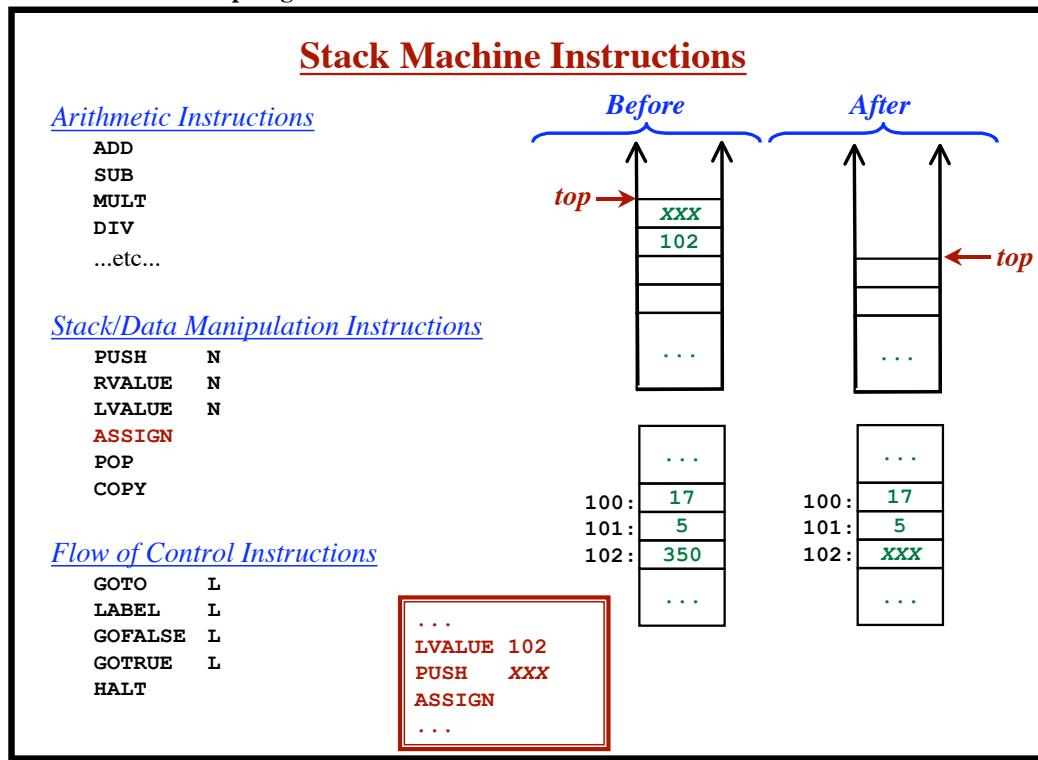
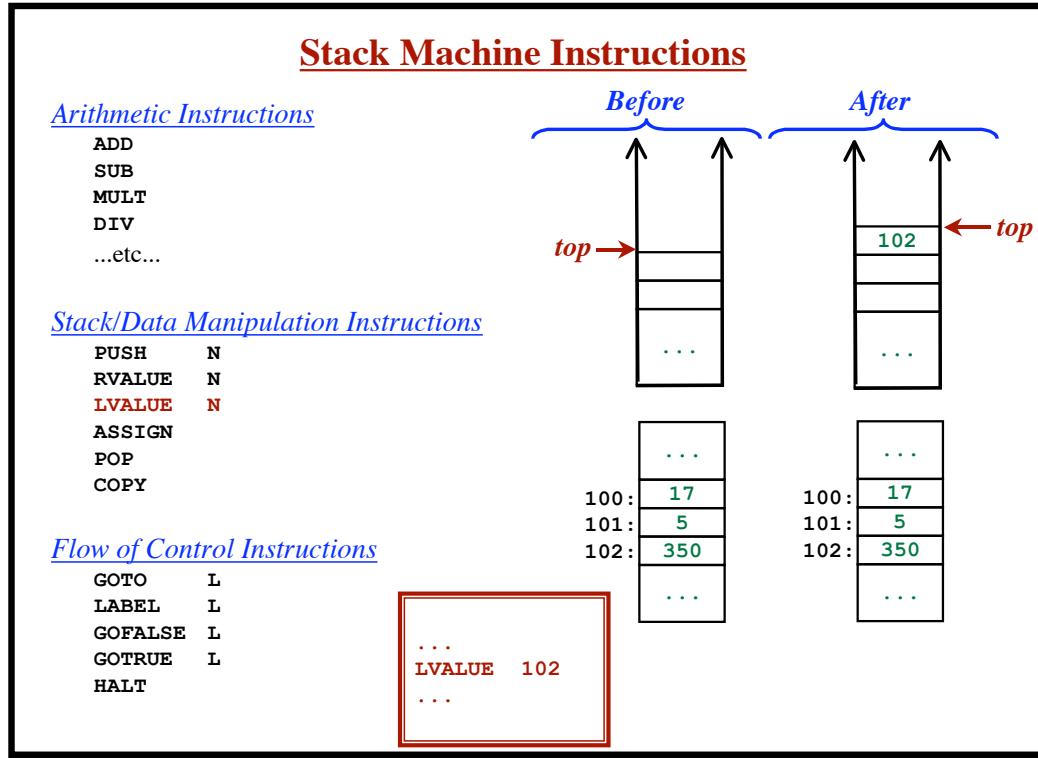
PUSH N
RVALUE N
LVALUE N
ASSIGN
POP
COPY

Flow of Control Instructions

GOTO L
LABEL L
GOFALSE L
GOTRUE L
HALT



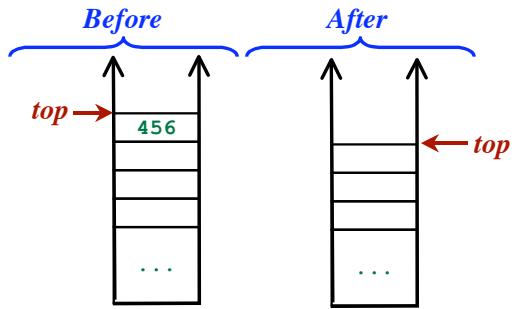




Stack Machine Instructions

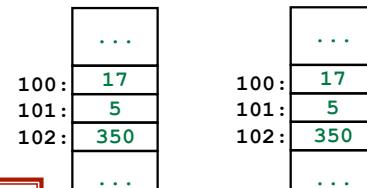
Arithmetic Instructions

ADD
SUB
MULT
DIV
...etc...



Stack/Data Manipulation Instructions

PUSH	N
RVALUE	N
LVALUE	N
ASSIGN	
POP	
COPY	



Flow of Control Instructions

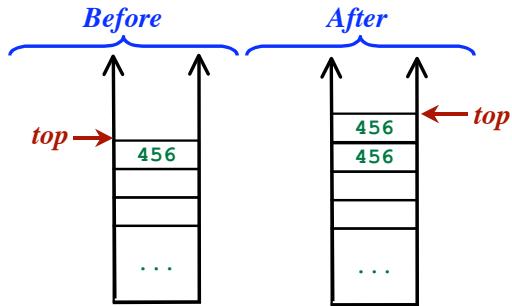
GOTO	L
LABEL	L
GOFALSE	L
GOTRUE	L
HALT	



Stack Machine Instructions

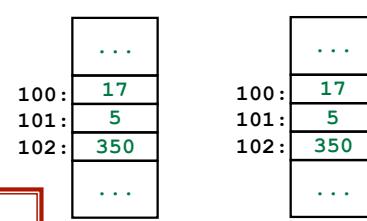
Arithmetic Instructions

ADD
SUB
MULT
DIV
...etc...



Stack/Data Manipulation Instructions

PUSH	N
RVALUE	N
LVALUE	N
ASSIGN	
POP	
COPY	



Flow of Control Instructions

GOTO	L
LABEL	L
GOFALSE	L
GOTRUE	L
HALT	



Flow of Control

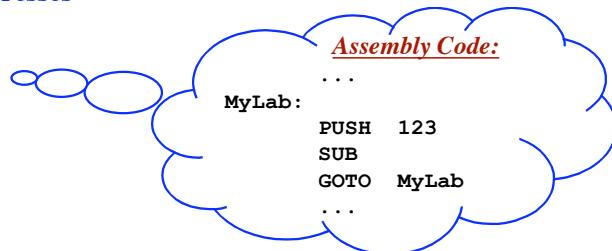
Option 1: Absolute Addresses

```
...  
1004: PUSH 123  
1005: SUB  
1006: GOTO 1004  
...
```

Flow of Control

Option 1: Absolute Addresses

```
...  
1004: PUSH 123  
1005: SUB  
1006: GOTO 1004  
...
```



Flow of Control

Option 1: Absolute Addresses

```
...  
1004: PUSH 123  
1005: SUB  
1006: GOTO 1004  
...
```

Option 2: Relative Addresses

```
...  
1004: PUSH 123  
1005: SUB  
1006: GOTO -2  
...
```

Flow of Control

Option 1: Absolute Addresses

```
...  
1004: PUSH 123  
1005: SUB  
1006: GOTO 1004  
...
```

Option 2: Relative Addresses

```
...  
1004: PUSH 123  
1005: SUB  
1006: GOTO -2  
...
```



Flow of Control

Option 1: Absolute Addresses

```
...
1004: PUSH 123
1005: SUB
1006: GOTO 1004
...
```

Option 2: Relative Addresses

```
...
1004: PUSH 123
1005: SUB
1006: GOTO -2
...
```

Option 3: Symbolic Labels

```
...
1003: LABEL MyLab
1004: PUSH 123
1005: SUB
1006: GOTO MyLab
...
```

Flow of Control

Option 1: Absolute Addresses

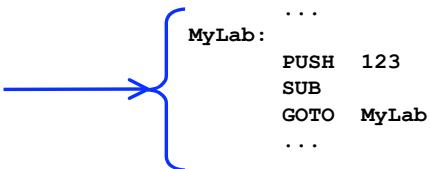
```
...
1004: PUSH 123
1005: SUB
1006: GOTO 1004
...
```

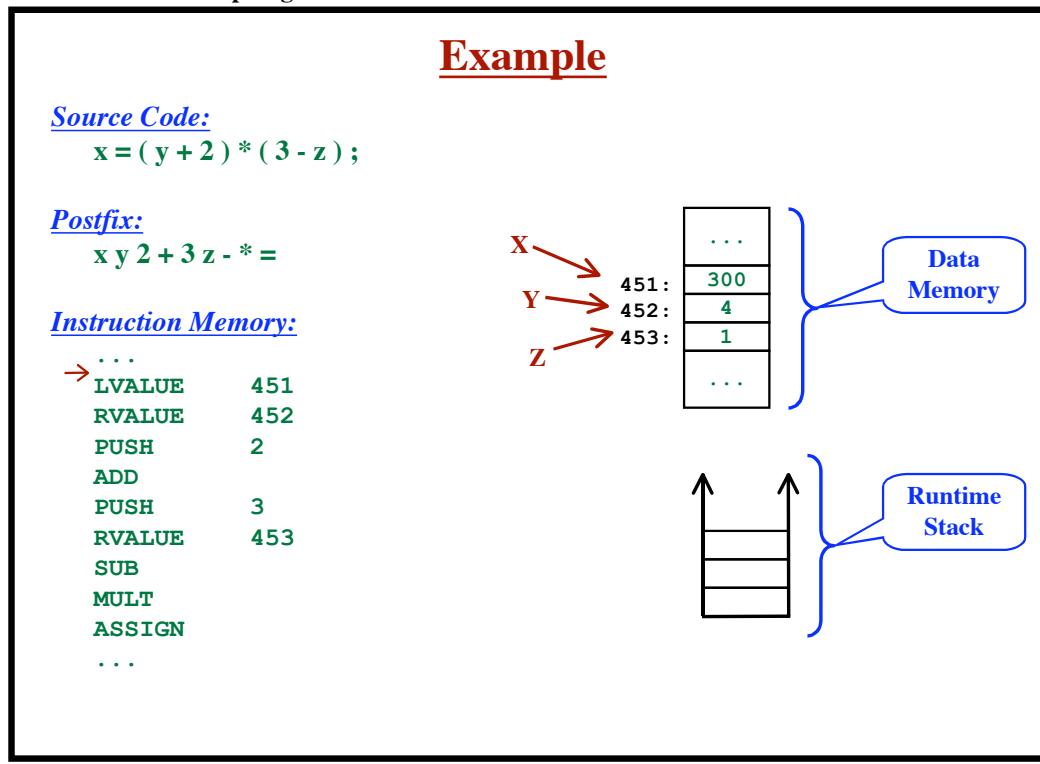
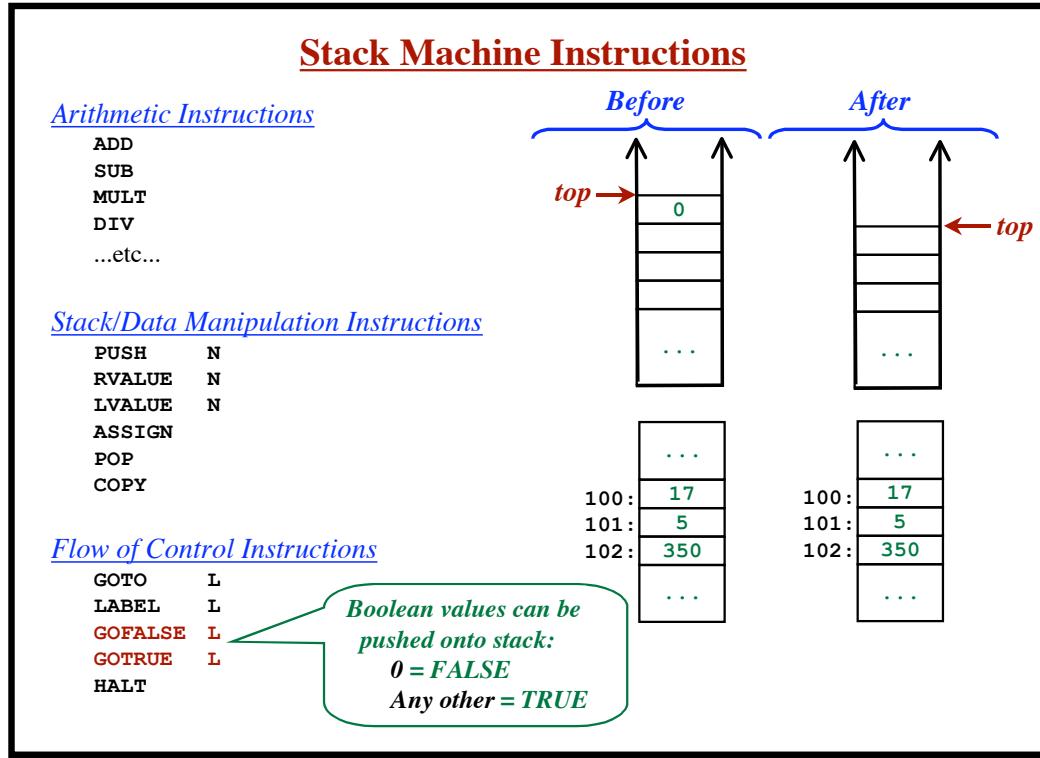
Option 2: Relative Addresses

```
...
1004: PUSH 123
1005: SUB
1006: GOTO -2
...
```

Option 3: Symbolic Labels

```
...
1003: LABEL MyLab
1004: PUSH 123
1005: SUB
1006: GOTO MyLab
...
```





Example

Source Code:

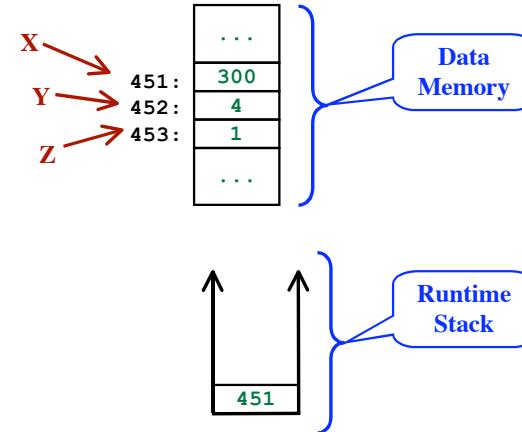
```
x = ( y + 2 ) * ( 3 - z ) ;
```

Postfix:

```
x y 2 + 3 z - * =
```

Instruction Memory:

```
...
→ LVALUE    451
RVALUE     452
PUSH      2
ADD
PUSH      3
RVALUE     453
SUB
MULT
ASSIGN
...
```



Example

Source Code:

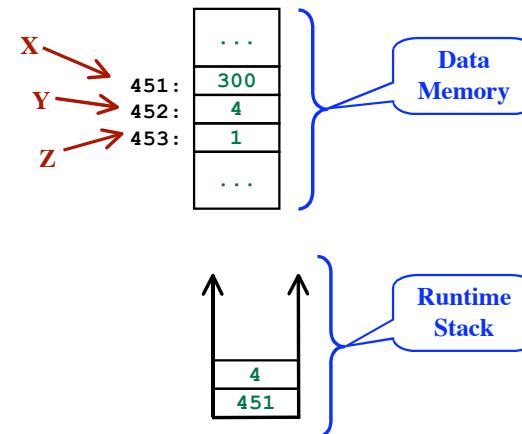
```
x = ( y + 2 ) * ( 3 - z ) ;
```

Postfix:

```
x y 2 + 3 z - * =
```

Instruction Memory:

```
...
→ RVALUE    452
PUSH      2
ADD
PUSH      3
RVALUE     453
SUB
MULT
ASSIGN
...
```



Example

Source Code:

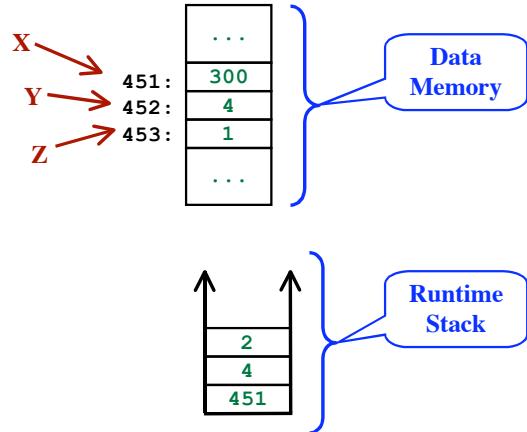
```
x = ( y + 2 ) * ( 3 - z );
```

Postfix:

```
x y 2 + 3 z - * =
```

Instruction Memory:

...	
LVALUE	451
RVALUE	452
PUSH	2
→ ADD	
PUSH	3
RVALUE	453
SUB	
MULT	
ASSIGN	
...	



Example

Source Code:

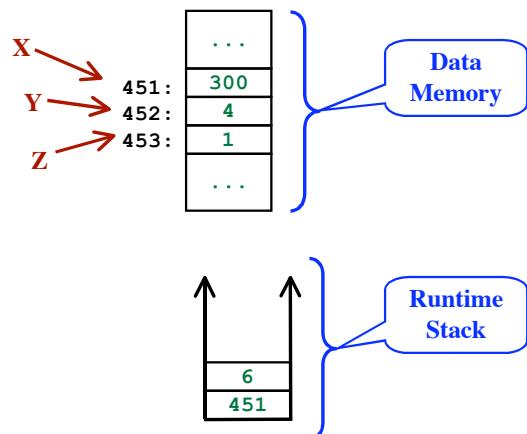
```
x = ( y + 2 ) * ( 3 - z );
```

Postfix:

```
x y 2 + 3 z - * =
```

Instruction Memory:

...	
LVALUE	451
RVALUE	452
PUSH	2
→ ADD	
PUSH	3
RVALUE	453
SUB	
MULT	
ASSIGN	
...	



Example

Source Code:

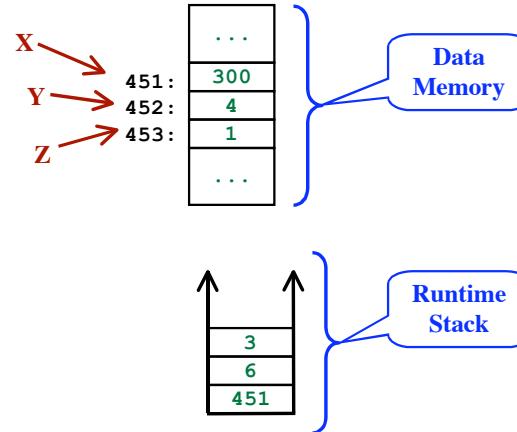
```
x = ( y + 2 ) * ( 3 - z );
```

Postfix:

```
x y 2 + 3 z - * =
```

Instruction Memory:

...	
LVALUE	451
RVALUE	452
PUSH	2
ADD	
PUSH	3
RVALUE	453
SUB	
MULT	
ASSIGN	
...	



Example

Source Code:

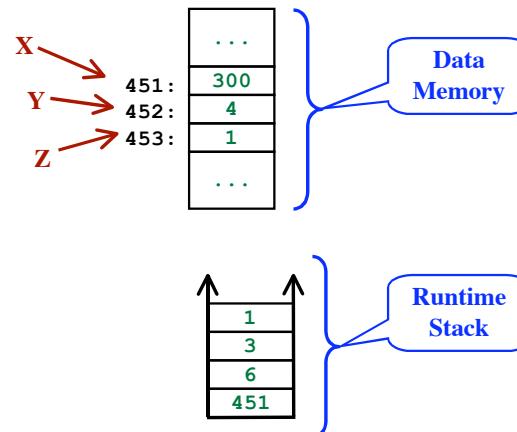
```
x = ( y + 2 ) * ( 3 - z );
```

Postfix:

```
x y 2 + 3 z - * =
```

Instruction Memory:

...	
LVALUE	451
RVALUE	452
PUSH	2
ADD	
PUSH	3
RVALUE	453
→ SUB	
MULT	
ASSIGN	
...	



Example

Source Code:

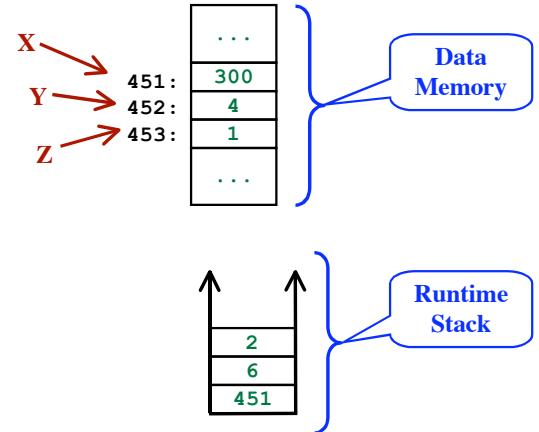
```
x = ( y + 2 ) * ( 3 - z );
```

Postfix:

```
x y 2 + 3 z - * =
```

Instruction Memory:

...	
LVALUE	451
RVALUE	452
PUSH	2
ADD	
PUSH	3
RVALUE	453
→ SUB	
MULT	
ASSIGN	
...	



Example

Source Code:

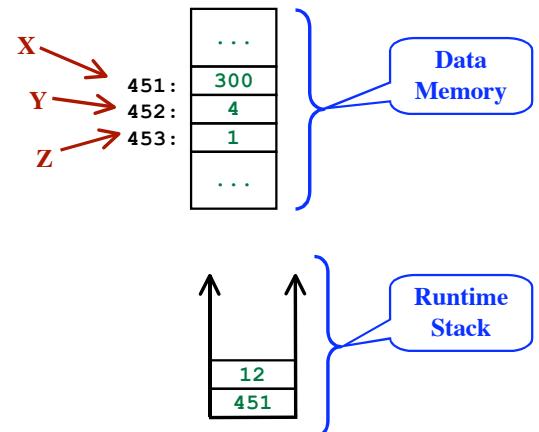
```
x = ( y + 2 ) * ( 3 - z );
```

Postfix:

```
x y 2 + 3 z - * =
```

Instruction Memory:

...	
LVALUE	451
RVALUE	452
PUSH	2
ADD	
PUSH	3
RVALUE	453
→ SUB	
MULT	
ASSIGN	
...	



Example

Source Code:

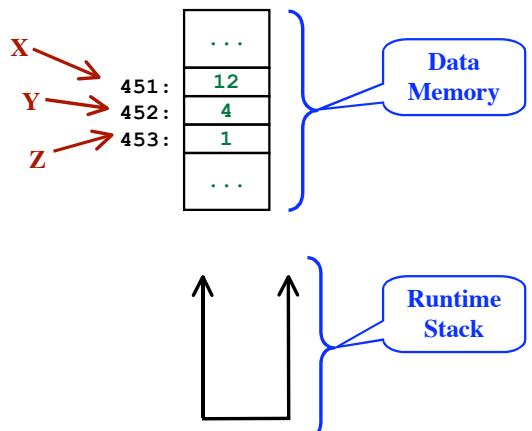
```
x = ( y + 2 ) * ( 3 - z );
```

Postfix:

```
x y 2 + 3 z - * =
```

Instruction Memory:

...	
LVALUE	451
RVALUE	452
PUSH	2
ADD	
PUSH	3
RVALUE	453
SUB	
MULT	
ASSIGN	
...	



Producing Translations

Target: Code for Abstract Stack MachineParseExpr ()

Parses an expression
... and produces the code for it.

ParseStmt ()

Parses a statement
... and produces the code for it
... using ParseExpr and ParseStmt recursively.

Assignment Stmt:

ID = Expr ;

For example: (X-3)*Y

RVALUE	X
PUSH	3
SUB	
RVALUE	Y
MULT	

Translation:

LVALUE ID

... *Code for Expr...*

ASSIGN

Translating a WHILE statement

Source:

```

...
while Expr do
    Stmts
endWhile
...
```

Translation:

```

...
LABEL Lab_43
    ... Code for Expr...
GOFALSE Lab_44
    ... Code for Stmts...
GOTO Lab_43
LABEL Lab_44
...
```

Generating Unique Labels

Function called: NewLabel

Returns a hitherto unused label.

Example:

```

Lab_17
Lab_18
Lab_19
...
```

Helper Function: EMIT()

Function: Emit()

Passed:

- An op-code
- Additional argument, if any

Writes one instruction to the output.

- To “stdout”
- To internal storage area
→ internal representation → target code → output file

Example of compiler code:

```

...
lab = NewLabel ();
Emit ("label", lab);
...
Emit ("goto", lab);
...
```

Translating Statements

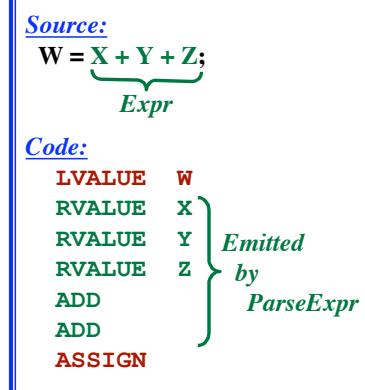
```

Stmt  → ID “=” Expr “;”
      → while Expr do Stmts endWhile
      → if Expr then Stmts else Stmts endIf
      → ...
Expr   → ...
  
```

Translating Statements

```

Stmt  → ID “=” Expr “;”
      → while Expr do Stmts endWhile
      → if Expr then Stmts else Stmts endIf
      → ...
Expr   → ...
  
```



Translating Statements

```

Stmt   → ID “=” Expr “;”
       → while Expr do Stmts endWhile
       → if Expr then Stmts else Stmts endIf
       → ...
Expr   → ...
  
```

Translation Scheme for ASSIGN-STMT:

```

Stmt   → ID
       { Emit ("LVALUE", ID.svalue) }
       “=”
Expr   { Emit ("ASSIGN") }
       “;”
  
```

Source:

$$W = \underbrace{X + Y + Z}_{Expr}$$

Code:

LVALUE	W	}
RVALUE	X	
RVALUE	Y	
RVALUE	Z	
ADD	ADD	
ADD	ASSIGN	

Emitted by ParseExpr

Translating Statements

```

Stmt   → ID “=” Expr “;”
       → while Expr do Stmts endWhile
       → if Expr then Stmts else Stmts endIf
       → ...
Expr   → ...
  
```

Source:

$$\text{while A-B do} \\ \quad X=Y; \\ \text{endWhile}$$

Code:

LABEL	Lab_4	}
RVALUE	A	
RVALUE	B	
SUB		
GOFALSE	Lab_5	
LVALUE	X	
RVALUE	Y	
ASSIGN		
GOTO	Lab_4	
LABEL	Lab_5	

Emitted by ParseExpr

Emitted by ParseStmts

Translating Statements

```

Stmt   → ID “=” Expr “;”
       → while Expr do Stmts endWhile
       → if Expr then Stmts else Stmts endIf
       → ...
Expr   → ...
  
```

Translation Scheme for WHILE-STM:

```

Stmt → while
      { topLabel = NewLabel()
        bottomLabel = NewLabel()
        Emit ("LABEL", topLabel) }

Expr
      { Emit ("GOFALSE", bottomLabel) }
      do Stmts endWhile
      { Emit ("GOTO", topLabel)
        Emit ("LABEL", bottomLabel) }
  
```

Source:
while A-B do
 X=Y;
endWhile

Code:
 LABEL Lab_4
 RVALUE A } *Emitted*
 RVALUE B } *by*
 SUB ParseExpr
 GOFALSE Lab_5
 LVALUE X } *Emitted*
 RVALUE Y } *by*
 ASSIGN ParseStmts
 GOTO Lab_4
 LABEL Lab_5

```

function ParseStmt ()
  var topLabel, bottomLabel: String
  if nextToken == ID then
    Emit ("LVALUE", token.svalue)
    MustHave (ID)
    MustHave ("=")
    ParseExpr ()
    Emit ("ASSIGN")
    MustHave (";")
  elseif nextToken == WHILE then
    MustHave (WHILE)
    topLabel = NewLabel ()
    bottomLabel = NewLabel ()
    Emit ("LABEL", topLabel)
    ParseExpr ()
    Emit ("GOFALSE", bottomLabel)
    MustHave (DO)
    ParseStmts ()
    MustHave (ENDWHILE)
    Emit ("GOTO", topLabel)
    Emit ("LABEL", bottomLabel)
  elseif
    ...
  endIf
endFunction
  
```

Stmt → ID
 { Emit ("LVALUE", ID.svalue) }
 “=”
Expr
 { Emit ("ASSIGN") }
 “;”
Stmt → while
 { topLabel = NewLabel ()
 bottomLabel = NewLabel ()
 Emit ("LABEL", topLabel) }
Expr
 { Emit ("GOFALSE", bottomLabel) }
 do Stmts endWhile
 { Emit ("GOTO", topLabel)
 Emit ("LABEL", bottomLabel) }

Short-Circuit Operators

if (i <= max) and (a[i] == -1) then ...

Do we need to evaluate the second expression?

b = Expr₁ and Expr₂



b = if Expr₁ then Expr₂ else FALSE endif

Short-Circuit Operators

if (i <= max) and (a[i] == -1) then ...

Do we need to evaluate the second expression?

b = Expr₁ and Expr₂



b = if Expr₁ then Expr₂ else FALSE endif

Translation:

```

...
... Code for Expr1...
COPY
GOFALSE Lab_43
POP
... Code for Expr2...
LABEL Lab_43
...

```

Short-Circuit Operators

if (i <= max) and (a[i] == -1) then ...

Do we need to evaluate the second expression?

b = Expr₁ and Expr₂

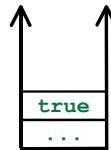


b = if Expr₁ then Expr₂ else FALSE endif

Translation:

```
...
→ ... Code for Expr1...
COPY
GOFALSE Lab_43
POP
... Code for Expr2...
LABEL Lab_43
...
```

Case 1: Expr-1 is true:



Short-Circuit Operators

if (i <= max) and (a[i] == -1) then ...

Do we need to evaluate the second expression?

b = Expr₁ and Expr₂

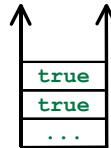


b = if Expr₁ then Expr₂ else FALSE endif

Translation:

```
...
→ ... Code for Expr1...
COPY
GOFALSE Lab_43
POP
... Code for Expr2...
LABEL Lab_43
...
```

Case 1: Expr-1 is true:



Short-Circuit Operators

```
if (i <= max) and (a[i] == -1) then ...
```

Do we need to evaluate the second expression?

b = Expr₁ and Expr₂

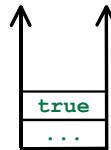


b = if Expr₁ then Expr₂ else FALSE endif

Translation:

```
...
... Code for Expr1...
COPY
→ GOFALSE Lab_43
POP
... Code for Expr2...
LABEL Lab_43
...
```

Case 1: Expr-1 is true:



Short-Circuit Operators

```
if (i <= max) and (a[i] == -1) then ...
```

Do we need to evaluate the second expression?

b = Expr₁ and Expr₂

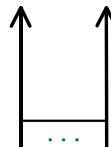


b = if Expr₁ then Expr₂ else FALSE endif

Translation:

```
...
... Code for Expr1...
COPY
GOFALSE Lab_43
→ POP
... Code for Expr2...
LABEL Lab_43
...
```

Case 1: Expr-1 is true:



Short-Circuit Operators

if (i <= max) and (a[i] == -1) then ...

Do we need to evaluate the second expression?

b = Expr₁ and Expr₂

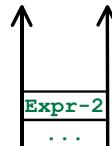


b = if Expr₁ then Expr₂ else FALSE endif

Translation:

```
...
... Code for Expr1...
COPY
GOFALSE Lab_43
POP
→LABEL Lab_43
...
... Code for Expr2...
```

Case 1: Expr-1 is true:



Short-Circuit Operators

if (i <= max) and (a[i] == -1) then ...

Do we need to evaluate the second expression?

b = Expr₁ and Expr₂

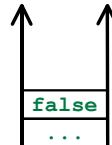


b = if Expr₁ then Expr₂ else FALSE endif

Translation:

```
...
→... Code for Expr1...
COPY
GOFALSE Lab_43
POP
... Code for Expr2...
LABEL Lab_43
...
```

Case 2: Expr-1 is false:



Short-Circuit Operators

if (i <= max) and (a[i] == -1) then ...

Do we need to evaluate the second expression?

b = Expr₁ and Expr₂

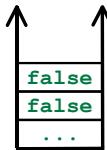


b = if Expr₁ then Expr₂ else FALSE endIf

Translation:

```
...
... Code for Expr1...
→ COPY
GOFALSE Lab_43
POP
... Code for Expr2...
LABEL Lab_43
...
```

Case 2: Expr-1 is false:



Short-Circuit Operators

if (i <= max) and (a[i] == -1) then ...

Do we need to evaluate the second expression?

b = Expr₁ and Expr₂

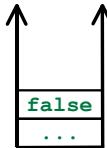


b = if Expr₁ then Expr₂ else FALSE endIf

Translation:

```
...
... Code for Expr1...
→ COPY
GOFALSE Lab_43
POP
... Code for Expr2...
LABEL Lab_43
...
```

Case 2: Expr-1 is false:



Short-Circuit Operators

And

$b = \text{Expr}_1 \text{ and } \text{Expr}_2$

Or

$b = \text{Expr}_1 \text{ or } \text{Expr}_2$

Conditional (ternary) operator

$b = \text{Expr}_1 ? \text{Expr}_2 : \text{Expr}_3$

Means: $b = (\text{if } \text{Expr}_1 \text{ then } \text{Expr}_2 \text{ else } \text{Expr}_3 \text{ endIf})$

Same as: `if Expr1 then
 b = Expr2
else
 b = Expr3
endIf`