ECE 271 Final Exam Guidelines

Your exam will be multiple-choice. There will be 30 questions and you will have 90 minutes to complete the exam. Be sure to bring a SCANTRON 882-E to the exam.

The exam is open-book/open notes. You are not allowed to have any device with an on/off switch within reach during the exam.

You are to have all datasheets that are posted on the course webpage. You will not be allowed to use someone else’s datasheets during the exam.

You should be familiar with the following

1. FFs (JK-FFs will not be covered)
2. module-*n* counters
3. special purpose counters (twisted-ring, etc.)
4. designing counters with specified sequences
5. FSM design (nothing on implication tables will be on the exam)
6. memories (type, usage, memory system design, timing)
7. PLDs (nothing on CPLDs. Be sure you understand FPGA architectures)
8. DFT concepts
9. LFSR design (including use as signature registers)
10. Transmission line effects
11. Reliability concepts
12. constructing part numbers

Material that will not be on the exam

1. Latches
2. Verilog

The exam is comprehensive.