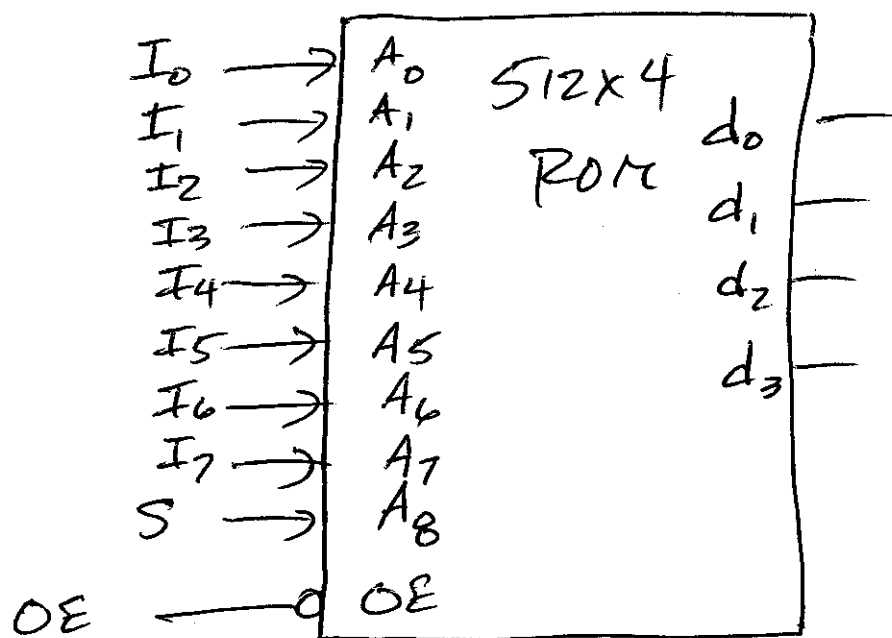


①

the 257 has 8 data inputs,
1 select input, an $\overline{OE}$ and
4 data outputs.

The $\overline{OE}$ can drive the $\overline{OE}$ pin
on the ROM, which leaves 9
inputs and 4 outputs

$$\therefore 2^9 \times 4 = 512 \times 4 \text{ ROM}$$



②

address: 18 bits

data: 16 Bits

③

8

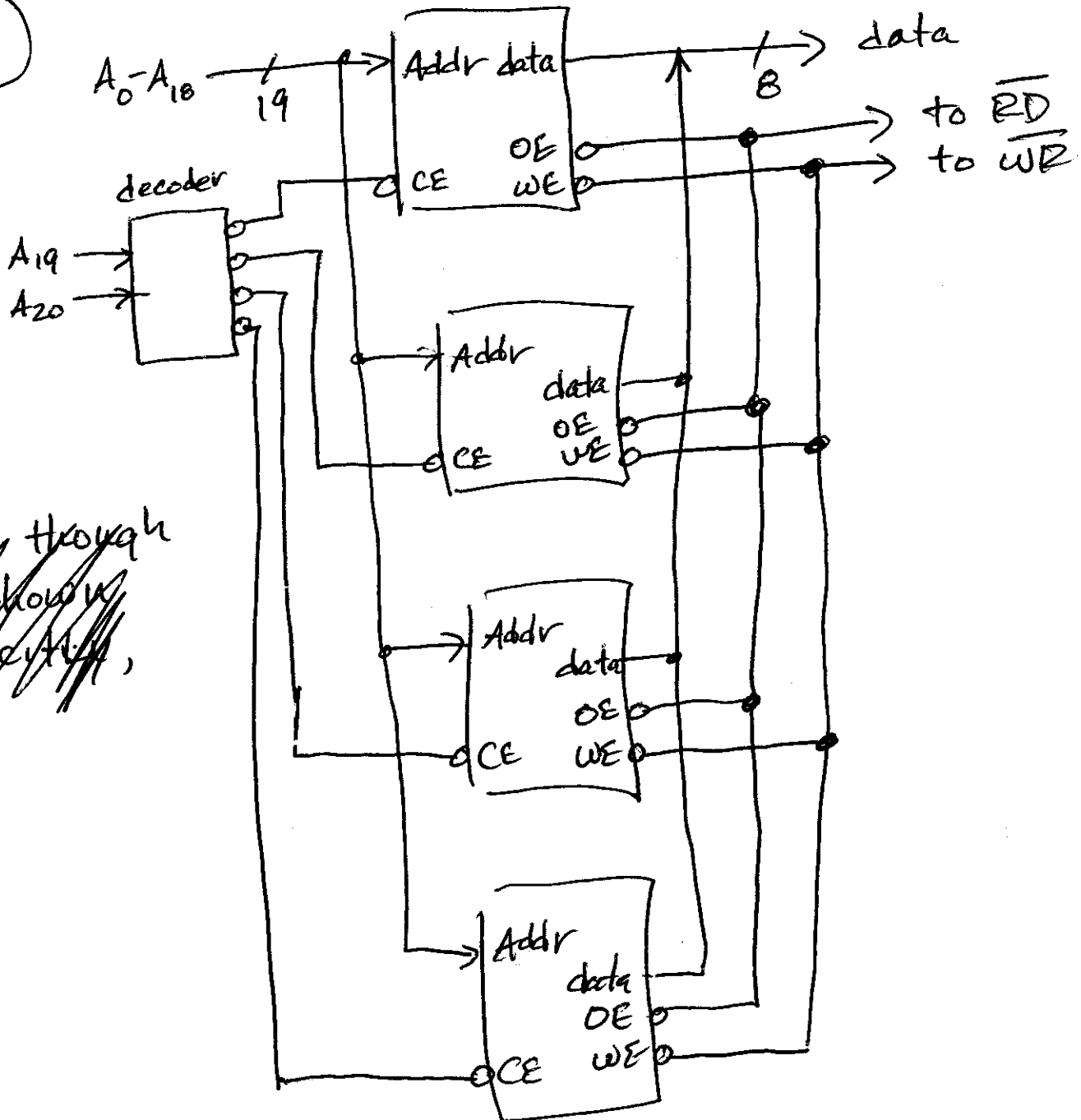
④

64K

⑤

60 ns

⑥



~~Note: though
not shown
explicitly,
OE~~

⑦

256 Bytes/sector

⑧

T_{RC} begins Before CE is asserted.

T_{AA} begins ~~start~~ while CE is already asserted