```
/**********************************************/
/* Sample linker command file */
/**********************************************/

file1.obj file2.obj file3.obj          /* input files */

/**********************************************/
/* Specify where physical memory is */
/**********************************************/

MEMORY
{
 ROM1:   orgin = 00000h   ,   length = 1000h

 ROM2:   orgin = 08000h   ,   length = 1000h

 RAM:    orgin = 0E000h   ,   length = 2000h
}



/**********************************************/
/* Assign modules to physical memory */
/**********************************************/

SECTIONS
{
   .text1:

   {
    file1.obj          /* define files that make the .text1 section */
    file2.obj
   }

   .text2:
   {
     file3.obj          /* define files that make the .text2 section */
   }

    .text1:  >  ROM1
    .text2:  >  ROM2
    .bss     >  RAM          /* uninitialized data memory */
    .data    >  RAM          /* initialized data memory */
}
```