

and #5(e, a, b);
or #4(out, e, c);

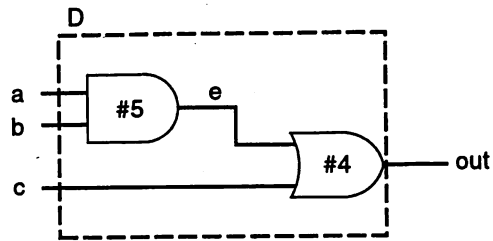


Figure 5-8 Module D

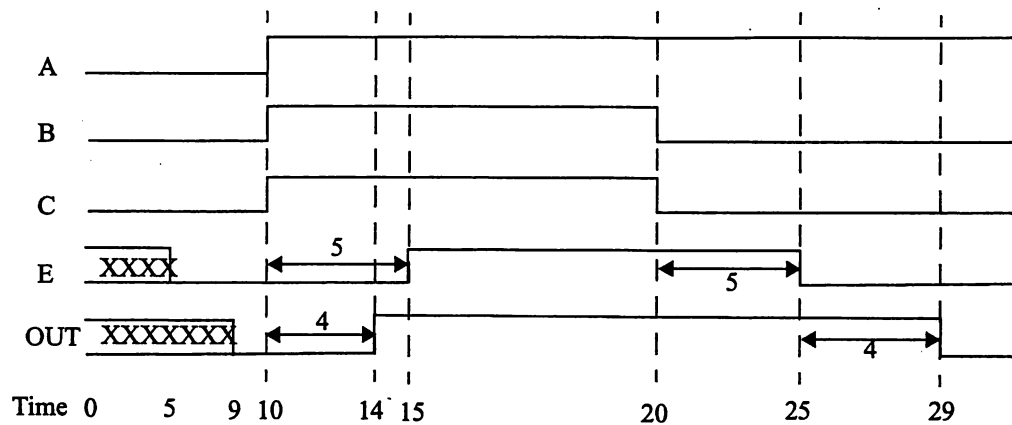


Figure 5-9 Waveforms for Delay Simulation