The final exam is scheduled for Thursday, December 10th from 1730-1900. The final is comprehensive⎯i.e., it covers all material discussed over the entire quarter.

The exam is multiple choice. **You must bring a SCANTRON 882-E to class**. If you do not put your answers on a SCANTRON 882-E, your exam will not be graded.

During the final you are prohibited from using any device that has an on/off switch (including, but not limited to, laptops, cellphones, iPhones, blackberries, PDAs, or kindles.)

The final will be open book/open notes. You may bring other Verilog books to class in addition to the textbook.

Since there will most likely be one or more questions on the Mentor Graphics design automation tools (the ones you saw in the videos), and you will not be able to view them during the exam, I strongly recommend you review them and take notes.