ECE 351 Final Exam Guidelines

The ECE 351 final exam is scheduled for Thursday, June 2nd at the regular class time. (Students who are planning on taking the exam at the testing center should make appropriate arrangements as soon as possible.

The exam will last 90 minutes and consist of 25 multiple-choice questions.

**YOU WILL NEED A SCANTRON 882-E FORM TO RECORD YOUR ANSWERS. YOU WILL ALSO NEED PENCILS.**

The exam is open book/open notes.

You may have a PDF reader (laptop, iPAD or whatever) to use during the exam.

The exam will be comprehensive although a bit more emphasis will be on FPGA prototyping concepts. You should be familiar with coding guidelines and configuration of the Virtex-5.

As an absolute minimum you should review the powerpoint slides associated with the Xilinx tutorial videos (and in particular the summary slides in those powerpoint files.)