

Soln

1. (4 pts) What do the following acronyms mean?

a. HDL

Hardware description Language

b. ASIC

Application specific integrated circuit

c. ASSP

application specific standard product

d. RTL

register transfer level

2. (2 pts) What property must all Verilog programs have?

must be synthesizable

3. (2 pts) What are the three inputs to a synthesizer?

- parts or component library
- HDL
- user constraints

4. (2 pts) What is a testbench?

a file defining the input test signals (or vectors) used during a circuit simulation