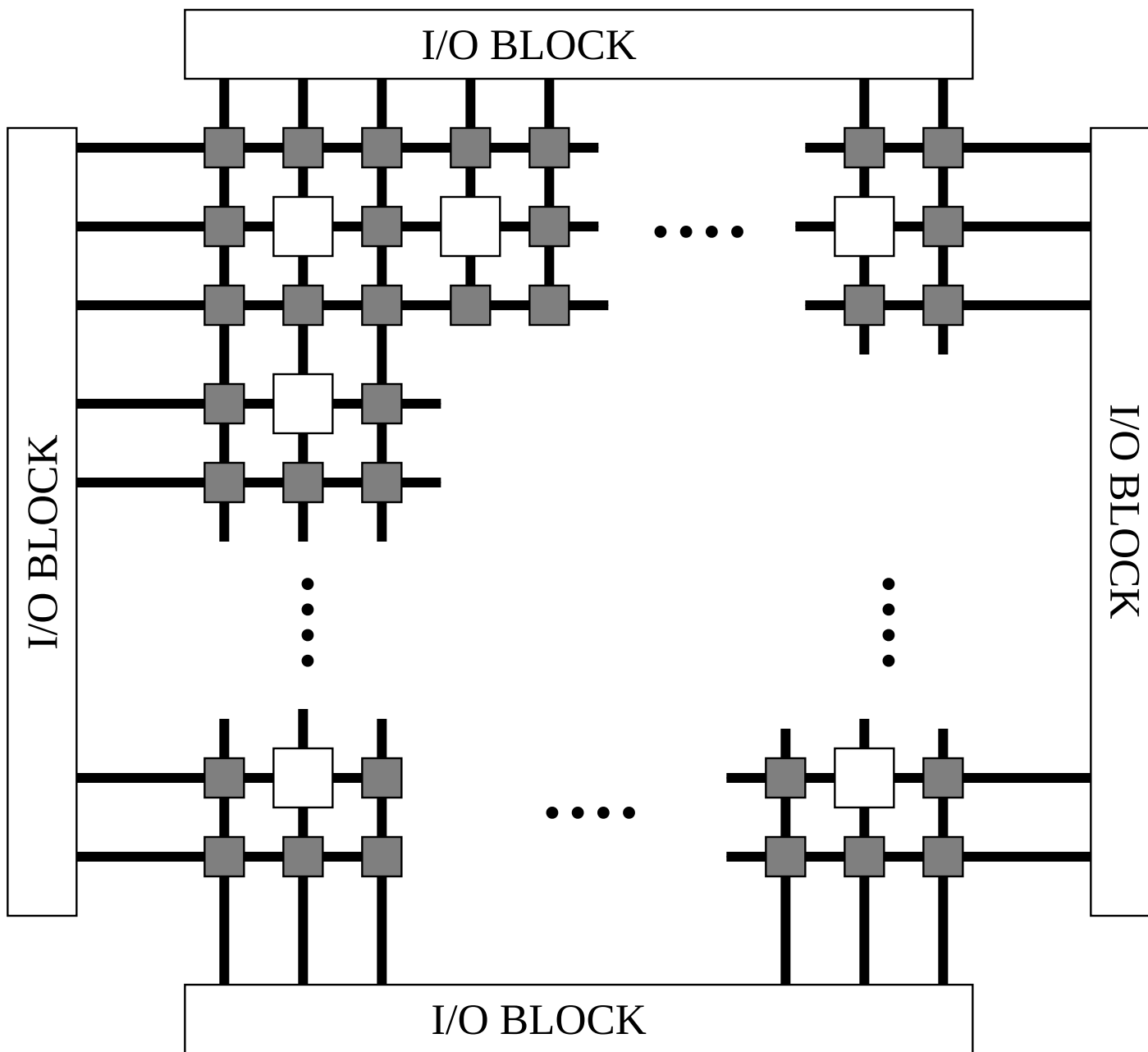




FPGA



LOGIC BLOCK



DATA SWITCH