Lecture Overview

Last Time
- BJT transistors in the linear region
- Linearity
- Superposition
- Exam 1 returned

This Time
- Source transformations
- Thévenin and Norton Equivalents
- Maximum Power Transfer

Miscellaneous

- TA OH Reminder
  - Office Hours: M 10-11, W 10-11
  - Review Sessions: T 10:30-11:30, T 5:30-6:30
  - In FAB 60-10 (Tektronix Lab) or adjacent rooms (60-06 or 60-08)
- IEEE Tutors available almost daily - check web site