## RTN Time-in-State Model ${ }^{1}$

## Alternative 1. Continuous Test

Consider a variable retention time bit which has an RTN waveform alternating between a high $(\mathrm{Hi},+)$ and a low ( $\mathrm{Lo},-$ ) state. In the case of a continuous test of duration $\Delta t$, if a bit is in the high state at the beginning of the test, then the test can detect any transition to the low state that occurs within the duration of the test. The probability that the test will detect only the high state of the bit is

$$
\begin{align*}
& P\left(\text { Continuous test with duration } \Delta t \text { starting at } t_{0} \text { detects only the Hi state }\right) \\
& =P\left(\text { Hi during interval } \Delta t \text { starting at } t_{0} \mid \mathrm{Hi} \text { at } t_{0}\right) P\left(\text { Hi at } t_{0}\right) \tag{1}
\end{align*}
$$

where the probability of finding a bit in a Hi state is

$$
\begin{equation*}
P\left(\text { Hi at } t_{0}\right)=\frac{\tau_{+}}{\tau_{+}+\tau_{-}} \equiv s \tag{2}
\end{equation*}
$$

Since this is a Poisson process the distribution of times until the next transition to low is

$$
\begin{equation*}
P\left(\text { Hi during interval } \Delta t \text { starting at } t_{0} \mid \text { Hi at } t_{0}\right)=\exp \left(-\frac{\Delta t}{\tau_{+}}\right) \tag{3}
\end{equation*}
$$

which is simply the distribution of durations in the high (+) state. Therefore

$$
P\left(\text { Measuring only Hi state for test with duration } \Delta t \text { starting at } t_{0} .\right)
$$

$$
\begin{equation*}
=s \exp \left(-\frac{\Delta t}{\tau_{+}}\right) \tag{4}
\end{equation*}
$$

## Alternative 2. Two Tests, Separated by $\Delta t$.

This alternative may be modeled using the autocorrelation function for RTN. The autocorrelation function for RTN is $P(+, t \mid+)$, the probability that the bit is Hi at time $t$, given that the bit is Hi at time 0 . That is $P(+, t++)$ is the probability that there are an even number of transitions between these times (including 0 transitions). Also, let $P(-, t \mid+)$, be the probability that the bit is Lo at time $t$, given that the bit is Hi at time 0 . That is, $P(-, t \mid+)$ is the probability that there are an odd number of transitions between these times. For RTN

$$
\begin{equation*}
P_{11}(+, t \mid+)+P(-, t \mid+)=1 \tag{5}
\end{equation*}
$$

[^0]and
\[

$$
\begin{equation*}
P(+, t+d t \mid+)=P(-, t \mid+) \frac{d t}{\tau_{-}}+P(+, t \mid+)\left(1-\frac{d t}{\tau_{+}}\right) \tag{6}
\end{equation*}
$$

\]

Eq. (6) expresses the fact that the change of the $P(+\mid+)$ probability in the interval $t$ to $t+d t$ is the sum of transitions into the + state from the - state at a constant rate of $1 / \tau_{-}$, and out of the + state at a constant rate of $1 / \tau_{+}$. The following differential equation follows from Eqs. (5) and (6)

$$
\begin{equation*}
\frac{d P(+, t \mid+)}{d t}+P(+, t \mid+)\left(\frac{1}{\tau_{+}}+\frac{1}{\tau_{-}}\right)=\frac{1}{\tau_{-}} \tag{7}
\end{equation*}
$$

which may be solved to give

$$
\begin{equation*}
P(+, t \mid+)=s+(1-s) \exp (-t / \hat{\tau}) \tag{8}
\end{equation*}
$$

where

$$
\begin{equation*}
s=\frac{\tau_{+}}{\tau_{+}+\tau_{-}} \quad \frac{1}{\hat{\tau}}=\frac{1}{\tau_{+}}+\frac{1}{\tau_{-}} \tag{9}
\end{equation*}
$$

In the DRAM test application the retention time waveform is tested instantaneously only at $t_{0}$ and at $t_{0}+\Delta t$, admitting the possibility that the waveform will transition to the low state any number of times between the measured high states. Using Eq. (8), the probability of finding the bit in the high state at $t_{0}+\Delta t$ given that it is in the high state at $t_{0}$ is:

$$
\begin{align*}
P\left(\text { Measuring Hi state instantaneously at } t_{0}+\Delta t\right. & \left.\mid \text { Hi at } t_{0}\right) \\
& =s+(1-s) \exp (-\Delta t / \hat{\tau}) . \tag{10}
\end{align*}
$$

So

$$
\begin{align*}
& P\left(\text { Two instantaneous tests at } t_{0} \text { and } t_{0}+\Delta t \text { detects only the Hi state }\right) \\
& \quad=P\left(\text { Measuring Hi state instantaneously at } t_{0}+\Delta t \mid \text { Hi at } t_{0}\right) P\left(\text { Hi at } t_{0}\right)  \tag{11}\\
& \quad=s^{2}+s(1-s) \exp (-\Delta t / \hat{\tau})
\end{align*}
$$

## Alternative 3. Multiple Tests, Separated by $\Delta t_{1}, \Delta t_{2}$,..

In this case the retention time waveform is tested instantaneously at $t_{0}, t_{1}=t_{0}+\Delta t_{1}$, $t_{2}=t_{0}+\Delta t_{1}+\Delta t_{2}, .$. This admits the possibility that the waveform will transition to the low state any number of times in between the measured high states. The probability that all measurements find the bit in a high state, given that it is in a high state at $t_{0}$ is

$$
\begin{align*}
& P\left(\text { Measuring Hi state instantaneously at all of }\left\{t_{i}\right\}_{i=1, n} \mid \text { Hi at } t_{0}\right) \\
& \quad=\prod_{i=1}^{n} P\left(\text { Measuring Hi state instantaneously at } t_{i} \mid \text { Hi at } t_{i-1}\right)  \tag{12}\\
& \quad=\prod_{i=1}^{n}\left[s+(1-s) \exp \left(-\Delta t_{i} / \hat{\tau}\right)\right]
\end{align*}
$$

So the probability that instantaneous tests at all of $\left\{t_{i}\right\}_{i=0, n}$ (at $n+1$ instants) will find the bit only in the high state is

$$
\begin{align*}
& P\left(\text { Measuring Hi state instantaneously at all of }\left\{t_{i}\right\}_{i=0, n}\right) \\
& \quad=P\left(\text { Measuring Hi state instantaneously at all of }\left\{t_{i}\right\}_{i=1, n} \mid \text { Hi at } t_{0}\right) P\left(\text { Hi at } t_{0}\right) \\
& \quad=s \prod_{i=1}^{n}\left[s+(1-s) \exp \left(-\Delta t_{i} / \hat{\tau}\right)\right] \tag{13}
\end{align*}
$$

## Example 1.

Suppose that $N+1$ tests are evenly spaced and that the separation between tests goes to zero so that it becomes a continuous test, with a total duration $\Delta t$. That is the separation of tests is $\Delta t / N$ as $N \rightarrow \infty$. In this case Eq. (13) becomes

$$
\begin{align*}
& P\left(\text { Measuring Hi state instantaneously at all of }\left\{t_{i}\right\}_{i=1, \infty}\right) \\
& \quad=\lim _{N \rightarrow \infty} s\left\{s+(1-s) \exp \left[-\frac{1}{\hat{\tau}} \frac{\Delta t}{N}\right]\right\}^{N} \simeq \lim _{N \rightarrow \infty} s\left\{s+(1-s)\left(1-\frac{1}{\hat{\tau}} \frac{\Delta t}{N}\right)\right\}^{N} \\
& \quad \simeq \lim _{N \rightarrow \infty} s\left(1-\frac{1-s}{\hat{\tau}} \frac{\Delta t}{N}\right)^{N}=\lim _{N \rightarrow \infty} s\left(1-\frac{\tau_{+}+\tau_{-}}{\tau_{+} \tau_{-}} \frac{\tau_{-}}{\tau_{+}+\tau_{-}} \frac{\Delta t}{N}\right)^{N}  \tag{14}\\
& \quad \simeq \lim _{N \rightarrow \infty} s\left\{1-\frac{\Delta t / \tau_{+}}{N}\right\}^{N}=s \exp \left(-\frac{\Delta t}{\tau_{+}}\right)
\end{align*}
$$

So the limiting case of Eq. (14) recovers Eq. (4) as it must.

## Example 2.

Suppose there are two instantaneous tests separated by $\Delta t$. From Eq. (11) the probability that both tests find the bit in the high state is

$$
\begin{equation*}
P(\text { Both bits high. })=s^{2}+s(1-s) \exp (-\Delta t / \hat{\tau}) \tag{15}
\end{equation*}
$$

and by symmetry the probability that both tests find the bits in the low state is

$$
\begin{equation*}
P(\text { Both bits low. })=(1-s)^{2}+s(1-s) \exp (-\Delta t / \hat{\tau}) . \tag{16}
\end{equation*}
$$

Since

$$
\begin{equation*}
P(\text { Both bits high. })+P(\text { Both bits low. })+P(\text { One bit high and the other low. })=1 \tag{17}
\end{equation*}
$$

then

$$
\begin{equation*}
P(\text { One bit high and the other low. })=2 s(1-s)[1-\exp (-\Delta t / \hat{\tau})] \tag{18}
\end{equation*}
$$

The high/low probability is irrespective of whether the first test or the second test finds the bit high.


[^0]:    ${ }^{1}$ Glenn Shirley, December 2012

