Midterm: in class Thursday May 15. Closed book, no notes, no calculators. RFIC Design. Review the course outcomes from week one, and sketch a design of an RFIC using what you have learned. Include as much detail as you can, but at least expected frequency range, RF specifications, dc current and voltage, a schematic, bond wire inductance considerations, a layout sketch, and as much information as you can regarding expected performance, risks, etc. Imagine this is a the written portion of an interview for an RFIC design position.

Midterm format: 4 pages:

1. Data sheet
2. Schematic with notes
3. Layout Sketch
4. Discussion of challenges