Tasks for this week:

Midterm on Wednesday. Continue oral progress report on individual receiver building blocks. Construction issues, how to measure each block individually.

Reverse Engineering an analog integrated circuit. Identify the key subsystems and components in this receiver on the photograph.

A 472 to 479 kHz IQ near-zero IF receiver integrated onto a single PC board. LO input upper right, RF input upper left, connections for off board baseband channel filter and output to DSP lower right. The IQ IF outputs are next to the ADE-1 mixers and the IQ inputs to the baseband analog signal processor are at the lower left. Optional roofing filters are on a separate board.

**Midterm In Class February 22.** Closed book, no notes, no calculators. Covering block diagrams and basic math of Superhet and IQ receive architectures with signal levels. Detailed schematic level description of your block.

Final class project. We will design and build individual contributions to the block diagram receiver. The final report will be a 4 page description, including design strategy, circuit details, measured results, and integration into the complete class project, with field testing. There will be as many blocks as students in the class, and they may be interconnected LEGO fashion into different receivers.