ECE 421/521 Study Guide #6 4 November 2015

Topic: Introduction to LTspice simulations of gain, Rin and Distortion

Tasks for this week:

AC and Transient simulations for primitive circuit study.

Homework--4 page report, due November 19.
1. Choose one circuit from the midterm
2. Analyze the circuit to come up with a working design
3. Simulate the circuit for gain, Rin, Distortion in LTspice
4. Build and measure the circuit in the lab, on a prototyping strip
5. Compare analysis, simulation, and measured results
6. Revise design so simulations, measurements, and analysis agree
7. Write a neat and concise report, with analysis, measured and simulated results

Note 1: This report adds to your total score, up to a possible 20 additional points.

Note 2: If your total class score (add the numbers on your exam) is 50 or more, you are encouraged to skip this assignment and instead apply the first 6 steps above to any or all of the midterm circuits, paying particular attention to distortion simulations and measurements in the lab.

Thank you for all of your hard work leading up to the midterm exam.

Final exam content:

Standard Analog IC Design Interview Questions.