ECE 323 Electronics Syllabus March 2015

Course Description


Schedule--Spring Quarter 2015
Tuesday and Thursday 12:00 13:50 Urban 204

Course Outcomes

Analyze and design single transistor and op-amp AC and DC feedback networks
A catalog of sinusoidal waveform generators
Logic families and gates
Small-signal transformers
Electronic multiplier/phase detector: Gilbert Cell, Diode Ring, and Exclusive OR
Interfacing between analog and digital circuits
Phase locked loop--acquiring lock
Phase locked loop--locked
Applying Electronics Engineering course work to open-ended projects
Introduction to Electronic Prototyping


Course structure: weekly study guides with homework assignments and prep material for upcoming exercises. Much of the important material in this class is directly related to work in the lab.

Grading: weekly ungraded and graded exercises, in-class mid-term, final project report, labs. Minimum two graded exercises in addition to mid-term and project.

In-class midterm exam Thursday May 14. Final project report dut 5:00 p.m. on the scheduled Final Exam day. No final exam.