ECE 323 Electronics III Syllabus April 2013

Draft Syllabus

Course Description


Schedule--Spring Quarter 2013
Tuesday and Thursday 12:00 - 1:50 on campus

Course Outcomes

Analyze and design single transistor and op-amp AC and DC feedback networks
A catalog of sinusoidal waveform generators
Logic families and gates
Electronic multiplier/phase detector: Gilbert Cell, Diode Ring, and Exclusive OR
Interfacing between analog and digital circuits
Phase locked loop -- acquiring lock
Phase locked loop -- locked
Applying Electronics Engineering course work to open-ended projects
Introduction to Electronic Prototyping


Course structure: weekly study guides with homework assignments and prep material for upcoming exercises.

Grading: weekly ungraded and graded exercises, in-class mid-term, final project report. Minimum of two graded exercises in addition to mid-term and project.

In-class midterm exam Thursday May 16, final project report due 5:00 p.m. the day of the scheduled final exam. No final exam.