Tasks for tenth week. Class will not meet the tenth week. Spend all of your lab and class time in the lab, working on at least 2 of the topics in lab 3.

Important dates coming up: Lab 3 Checkoff by TA during Lab periods the week of June 3-7. Lab 3 writeup Due Wednesday June 12. Lab 4 2 page writeup due Wednesday June 12. The homework problems below are due Friday June 7, and will be checked but not graded.

There is no final exam in this class. The final two weeks of the class are devoted to work on Lab 3, proposed lab 4, and exploring the sections of the text book that support the material we have studied in 321 322 and 323 this year.

Note: if you plan to take ECE 421 and ECE422, please keep your textbook. It is an excellent introduction/review of the most important topics in Analog Electronics. You will also find it useful in support of your future electronic projects, and the clear, basic treatment of basic transistor circuits is excellent material for review before technical interviews.

Take a few minutes to review the course outcomes for 323, and keep them in mind as you write up Lab 3 and propose future explorations of 323 topics in Lab 4.

ECE323 Course Outcomes

Analyze and design single transistor and op-amp AC and DC feedback networks
A catalog of sinusoidal waveform generators
Logic families and gates
Electronic multiplier/phase detector: Gilbert Cell, Diode Ring, and Exclusive OR
Interfacing between analog and digital circuits
Phase locked loop -- acquiring lock
Phase locked loop -- locked
Applying Electronics Engineering course work to open-ended projects
Introduction to Electronic Prototyping

Thank you for all your hard work this quarter. I have enjoyed working with each of you. Don’t hesitate to ask me questions via e-mail next week while I’m off campus, or over the summer if you have some interesting results in Lab 4. I look forward to seeing some of you in ECE421 in the fall, where we will really start to have some fun with 21st century analog electronics.