CS 201 Section 1 Homework 3 – due 6pm, Wednesday, January 26, 2000

1. Do Tanenbaum Exercise 5.2.

2. Do Tanenbaum Exercise 5.4.

3. Do Tanenbaum Exercise 5.6.

4. Do Tanenbaum Exercise 5.26.

5. Suppose that the SPARC instruction set did not include the `sethi` instruction. Describe a sequence of instructions that would allow you to load an arbitrary 32-bit constant into a register.