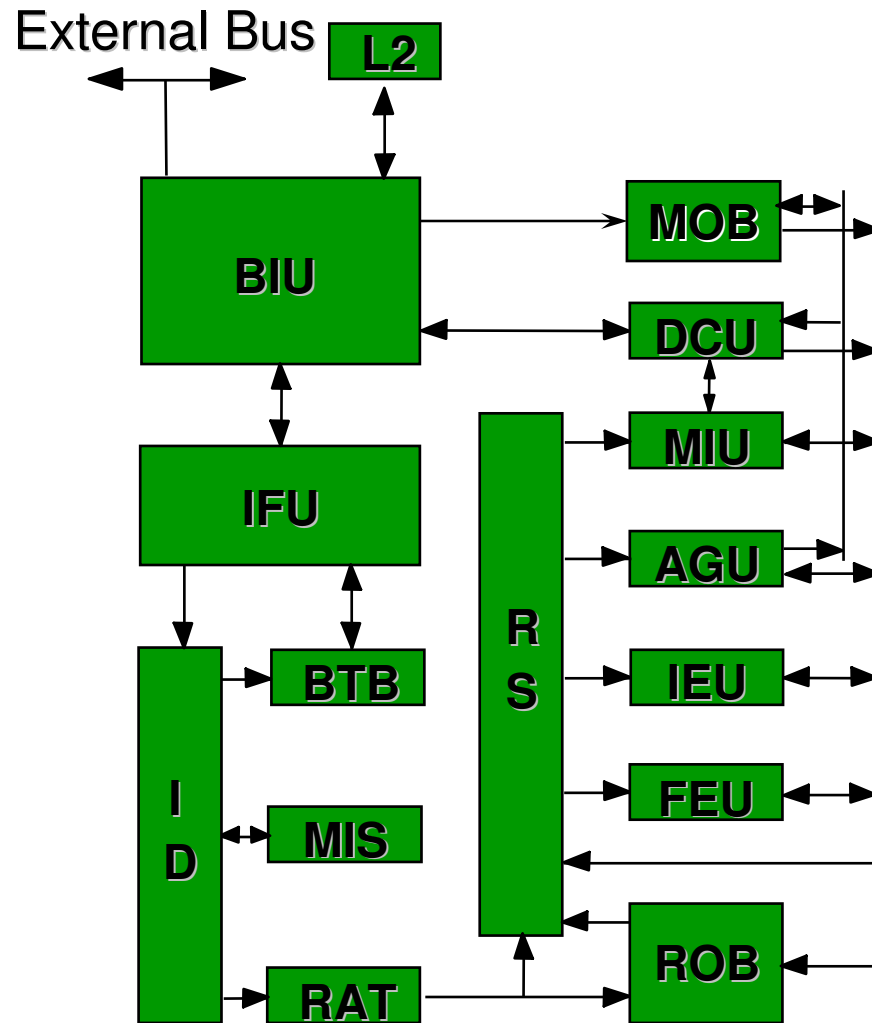
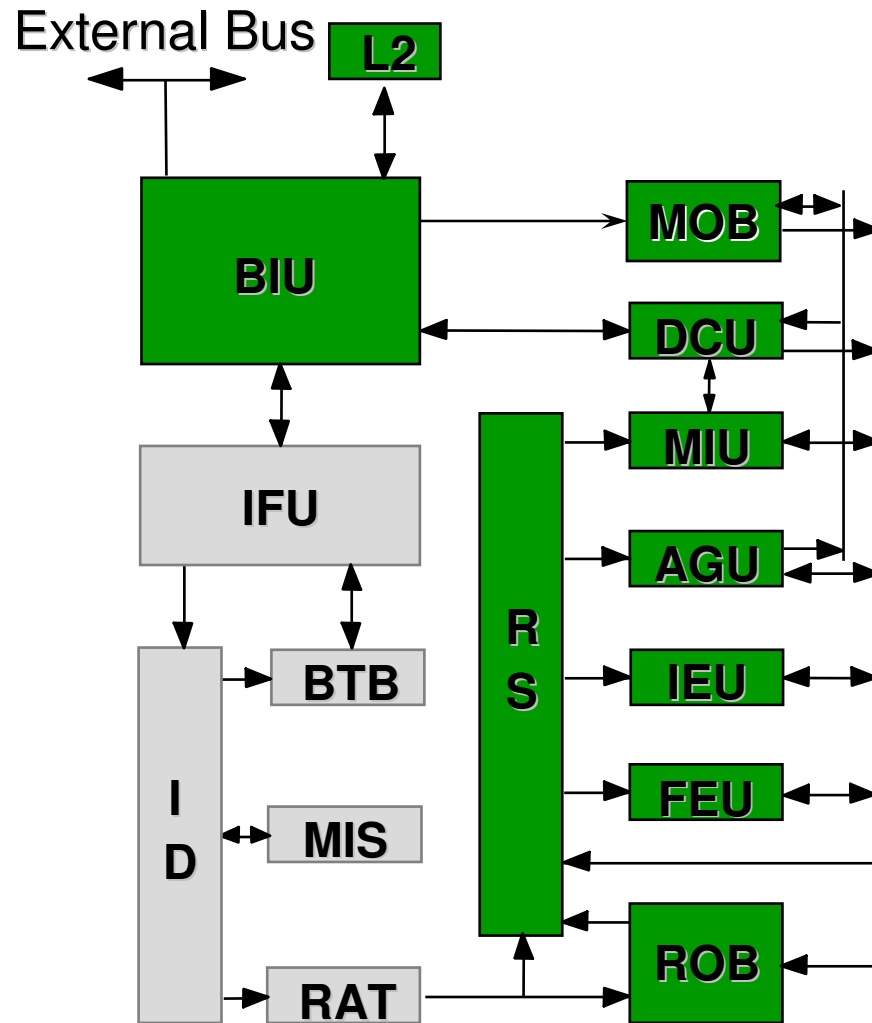


P6 CPU Microarchitecture



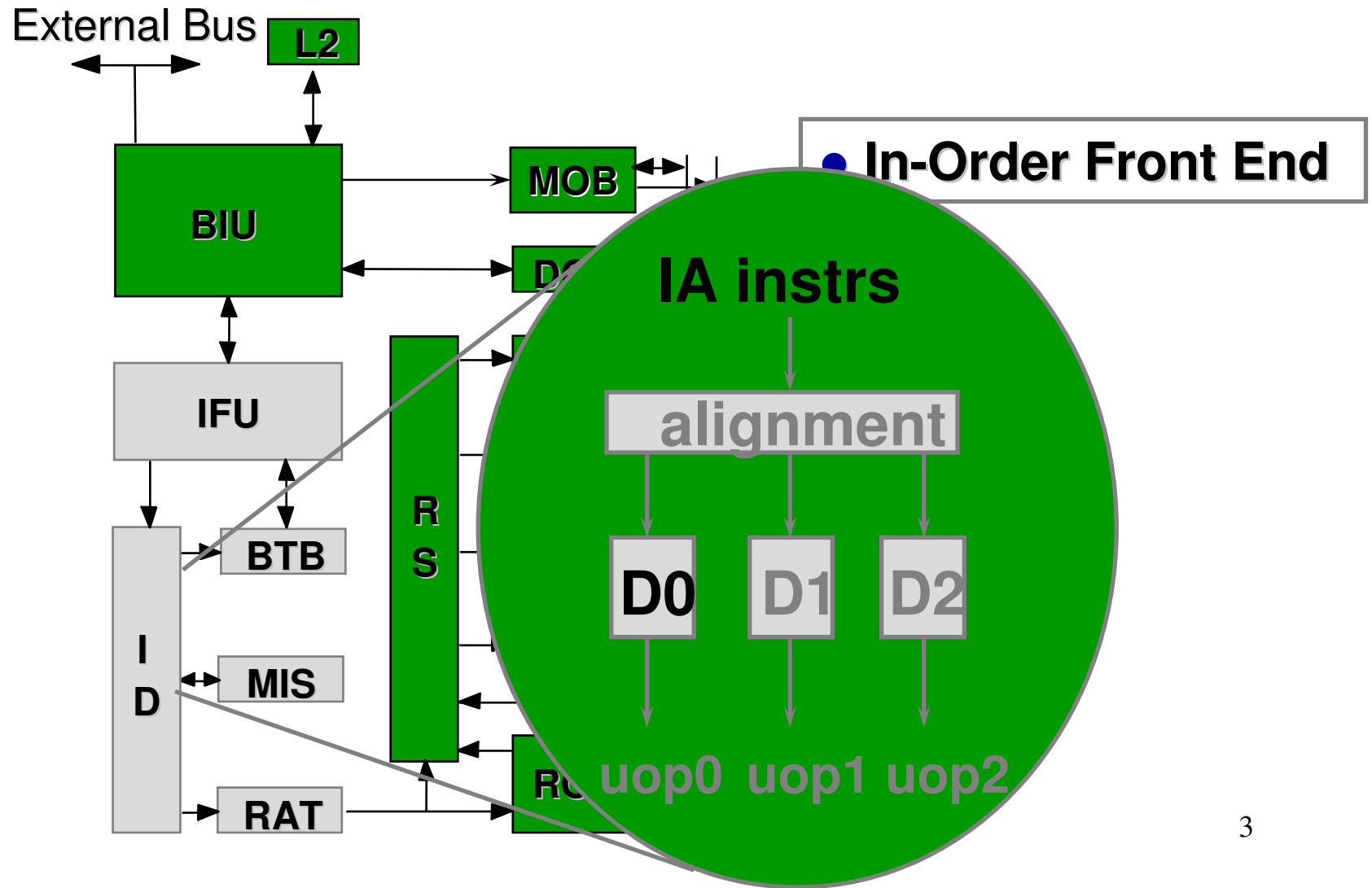
- In-Order Front End
- Out-of-order Core
- In-Order Retire

Microarchitecture

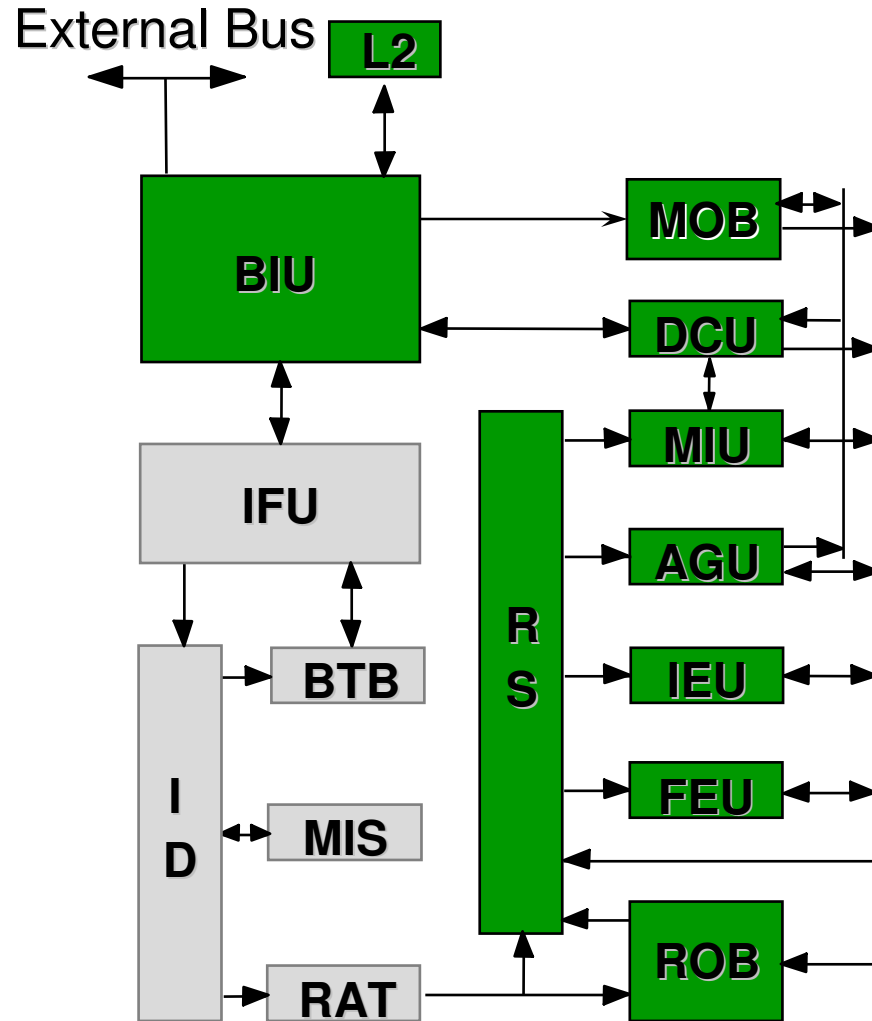


● In-Order Front End

Microarchitecture

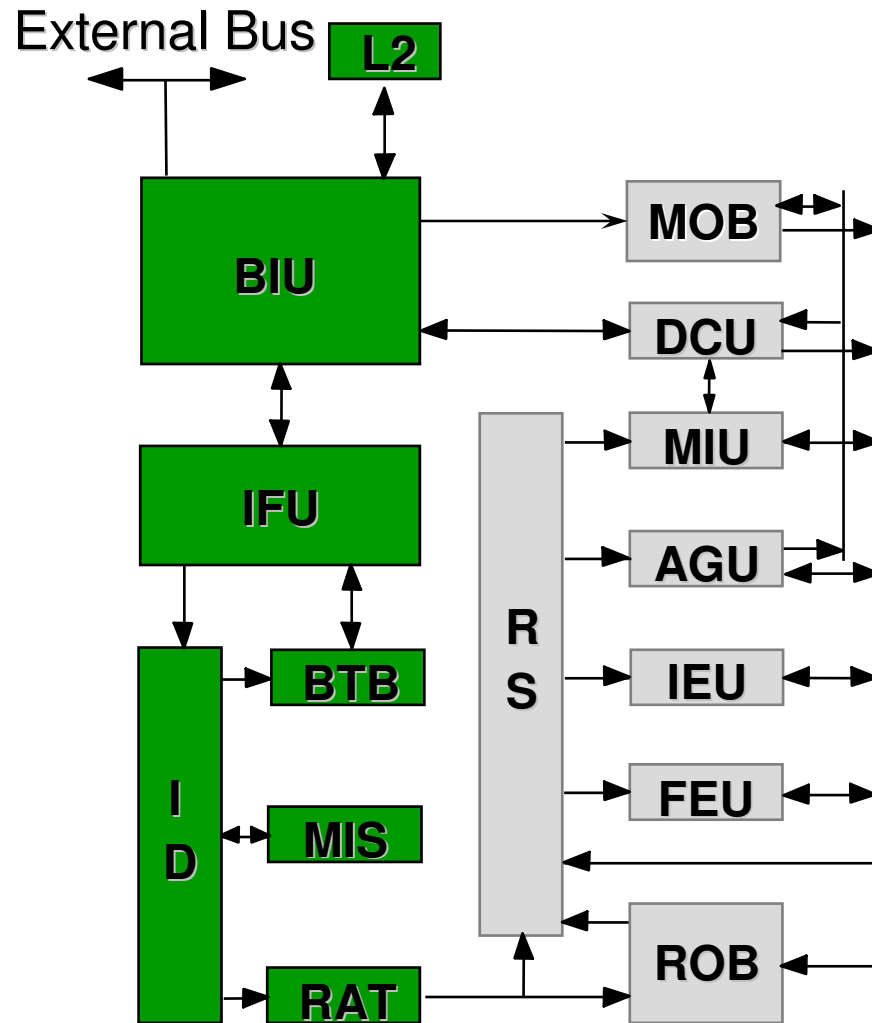


Microarchitecture



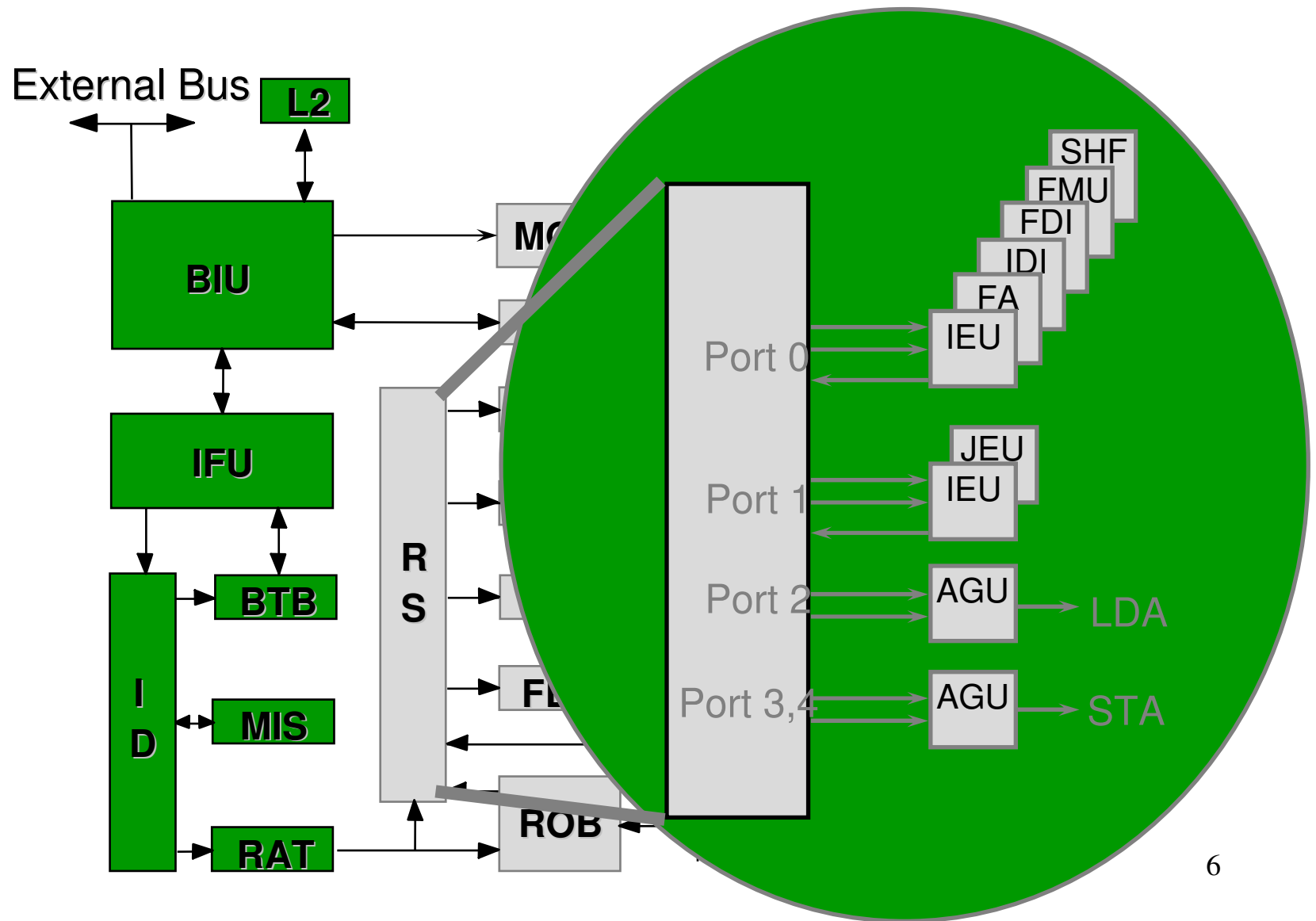
• In-Order Front End

Microarchitecture

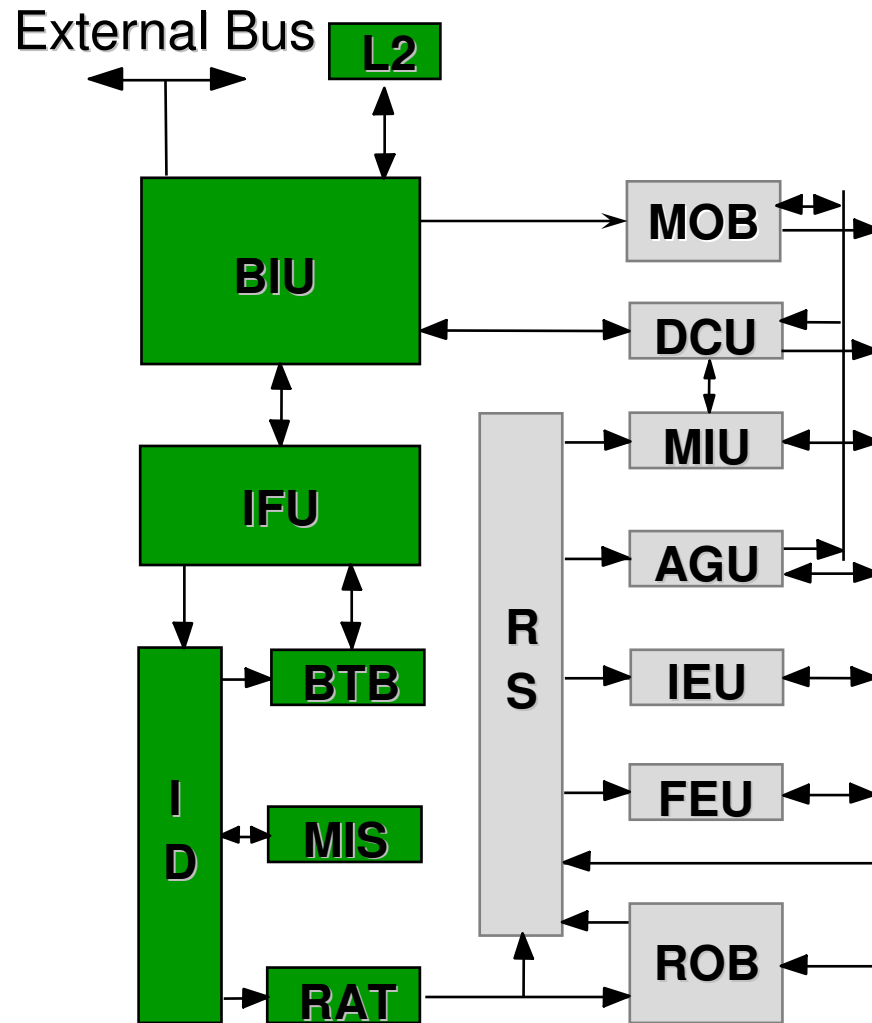


● Out-of-order Core

Microarchitecture

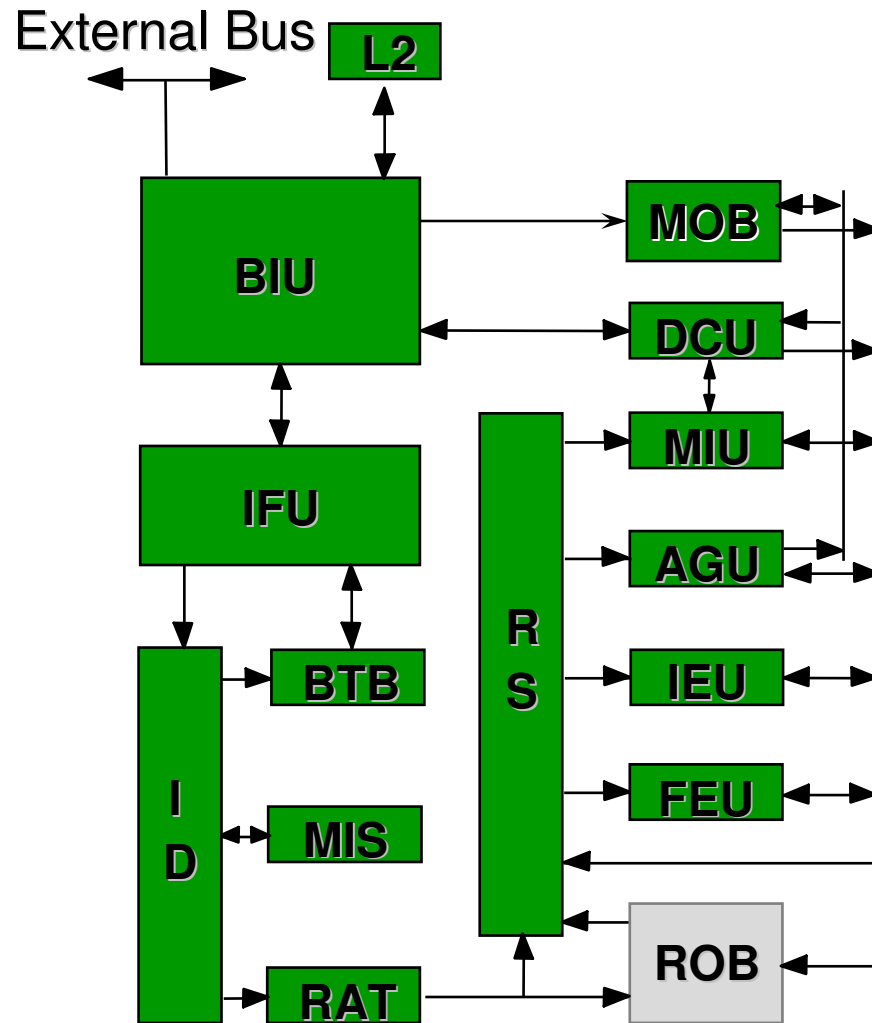


Microarchitecture



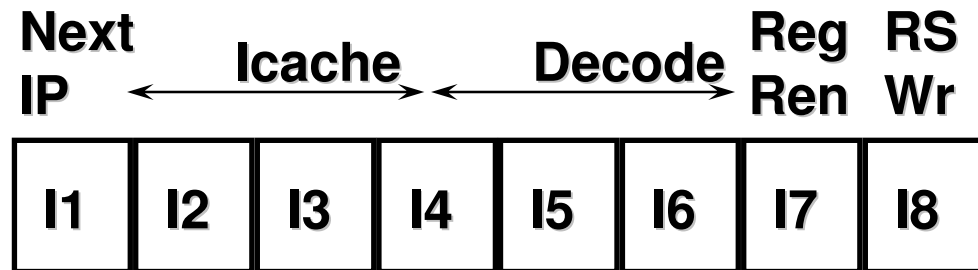
● Out-of-order Core

Microarchitecture



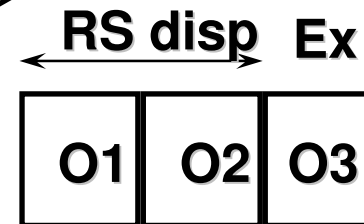
• In-Order Retire

Pipeline

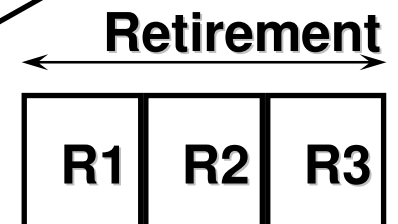


• In-Order Front End

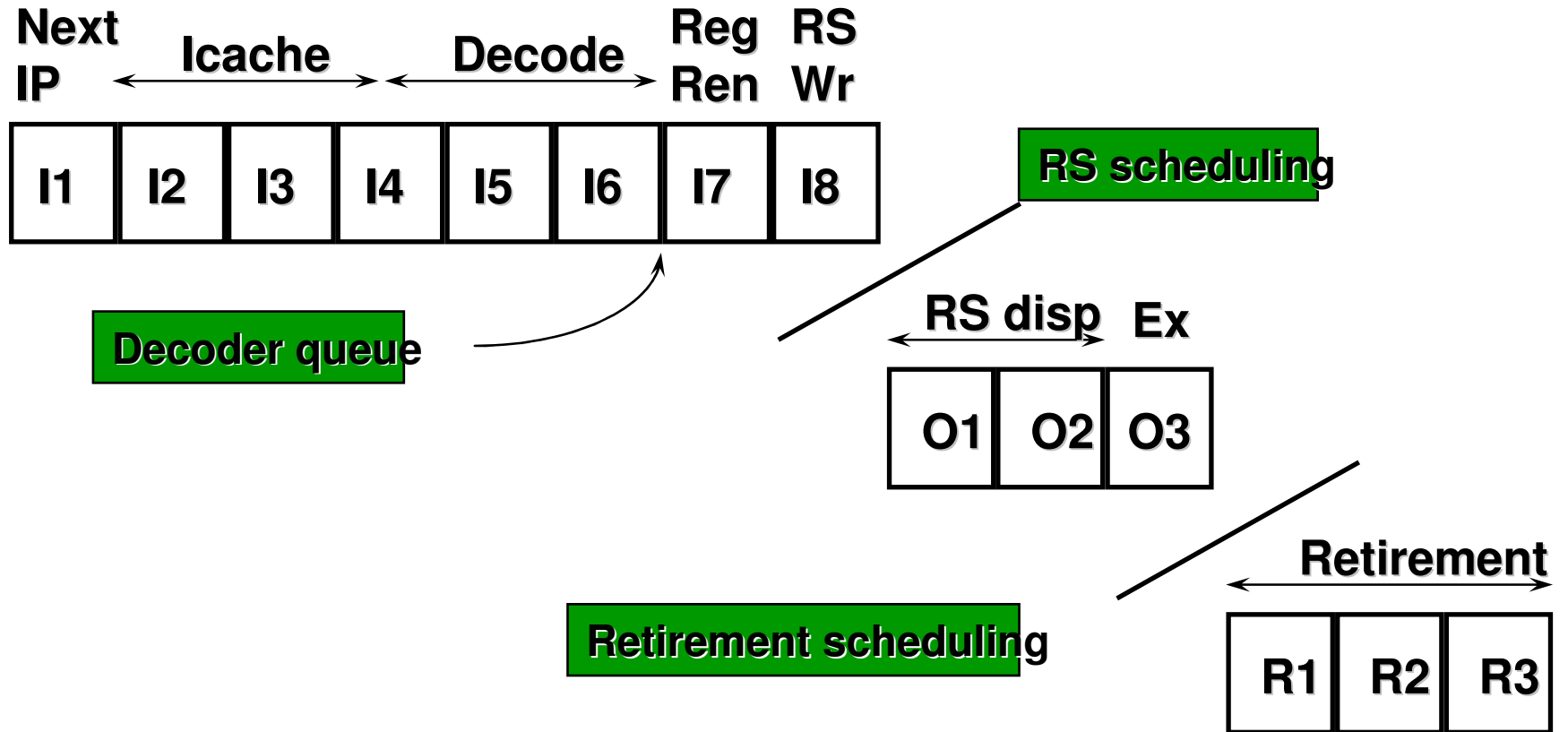
• Out-of-order Core



• In-order Retirement



Queuing



Reading Assignment

- Two VLIW papers.
- Itanium architecture.