

BLITZ Instruction Set – By Opcode

Op Code		Instruction	Format	Priv Inst	Condition Codes
(decimal)	(hex)				
0	00	nop	A		
1	01	wait	A	X	
2	02	debug	A		
3	03	cleari	A	X	
4	04	seti	A	X	
5	05	clearp	A	X	
6	06	setp	A	X	
7	07	clears	A	X	
8	08	reti	A	X	
9	09	ret	A		
10	0A	debug2	A		
32	20	ldptbr Rc	B	X	
33	21	ldptlr Rc	B	X	
64	40	call Ra+Rc	C		
65	41	jmp Ra+Rc	C		
66	42	be Ra+Rc	C		
67	43	bne Ra+Rc	C		
68	44	bl Ra+Rc	C		
69	45	ble Ra+Rc	C		
70	46	bg Ra+Rc	C		
71	47	bge Ra+Rc	C		
72	48				
73	49				
74	4A	bvs Ra+Rc	C		
75	4B	bvc Ra+Rc	C		
76	4C	bns Ra+Rc	C		
77	4D	bnc Ra+Rc	C		
78	4E	bss Ra+Rc	C		
79	4F	bsc Ra+Rc	C		
80	50	bis Ra+Rc	C		
81	51	bic Ra+Rc	C		
82	52	bps Ra+Rc	C		
83	53	bpc Ra+Rc	C		
84	54	push Rc, [--Ra]	C		
85	55	pop [Ra++], Rc	C		
86	56	readu Rc, Ra	C	X	
87	57	writeu Ra, Rc	C	X	
88	58	tset [Ra], Rc	C		
89	59	ftoi Fa, Rc	C		
90	5A	itof Ra, Fc	C		
91	5B	fcmp Fa, Fc	C		X
92	5C	fsqrt Fa, Fc	C		
93	5D	fneg Fa, Fc	C		
94	5E	fabs Fa, Fc	C		
96	60	add Ra, Rb, Rc	D		X
97	61	sub Ra, Rb, Rc	D		X
98	62	mul Ra, Rb, Rc	D		X
99	63	div Ra, Rb, Rc	D		X
100	64	sll Ra, Rb, Rc	D		X
101	65	srl Ra, Rb, Rc	D		X
102	66	sra Ra, Rb, Rc	D		X
103	67	or Ra, Rb, Rc	D		X
104	68	and Ra, Rb, Rc	D		X
105	69	andn Ra, Rb, Rc	D		X
106	6A	xor Ra, Rb, Rc	D		X
107	6B	load [Ra+Rb], Rc	D		
108	6C	loadb [Ra+Rb], Rc	D		
109	6D	loadv [Ra+Rb], Rc	D	X	
110	6E	loadbv [Ra+Rb], Rc	D	X	
111	6F	store Rc, [Ra+Rb]	D		
112	70	storeb Rc, [Ra+Rb]	D		
113	71	storev Rc, [Ra+Rb]	D	X	
114	72	storebv Rc, [Ra+Rb]	D	X	

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115	73	rem	Ra,Rb,Rc	D		X
116	74	fadd	Fa,Fb,Fc	D		
117	75	fsub	Fa,Fb,Fc	D		
118	76	fmul	Fa,Fb,Fc	D		
119	77	fdiv	Fa,Fb,Fc	D		
120	78	fload	[Ra+Rb],Fc	D		
121	79	fstore	Fc,[Ra+Rb]	D		
128	80	add	Ra,data16,Rc	E		X
129	81	sub	Ra,data16,Rc	E		X
130	82	mul	Ra,data16,Rc	E		X
131	83	div	Ra,data16,Rc	E		X
132	84	sll	Ra,data16,Rc	E		X
133	85	srl	Ra,data16,Rc	E		X
134	86	sra	Ra,data16,Rc	E		X
135	87	or	Ra,data16,Rc	E		X
136	88	and	Ra,data16,Rc	E		X
137	89	andn	Ra,data16,Rc	E		X
138	8A	xor	Ra,data16,Rc	E		X
139	8B	load	[Ra+data16],Rc	E		
140	8C	loadb	[Ra+data16],Rc	E		
141	8D	loadv	[Ra+data16],Rc	E	X	
142	8E	loadbv	[Ra+data16],Rc	E	X	
143	8F	store	Rc,[Ra+data16]	E		
144	90	storeb	Rc,[Ra+data16]	E		
145	91	storev	Rc,[Ra+data16]	E	X	
146	92	storebv	Rc,[Ra+data16]	E	X	
147	93	readu	Rc,[Ra+data16]	E	X	
148	94	writeu	[Ra+data16],Rc	E	X	
149	95	rem	Ra,data16,Rc	E		X
150	96	fload	[Ra+data16],Fc	E		
151	97	fstore	Fc,[Ra+data16]	E		
160	A0	call	data24	F		
161	A1	jmp	data24	F		
162	A2	be	data24	F		
163	A3	bne	data24	F		
164	A4	bl	data24	F		
165	A5	ble	data24	F		
166	A6	bg	data24	F		
167	A7	bge	data24	F		
168	A8					
169	A9					
170	AA	bvs	data24	F		
171	AB	bvc	data24	F		
172	AC	bns	data24	F		
173	AD	bnc	data24	F		
174	AE	bss	data24	F		
175	AF	bsc	data24	F		
176	B0	bis	data24	F		
177	B1	bic	data24	F		
178	B2	bps	data24	F		
179	B3	bpc	data24	F		
192	C0	sethi	data16,Rc	G		
193	C1	setlo	data16,Rc	G		
194	C2	ldaddr	data16,Rc	G		
195	C3	syscall	Rc+data16	G		